



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
ELL41	Analog Electronics Lab-II	--	--	2	--	--	1	1
		Examination Scheme						
		ISE			ESE		Total	
					Practical	Oral		
		40			10		10	60

Pre-requisite Course Codes		EL41 (Analog Electronics -II)
After successful completion of the course, student will be able to		
Course Outcomes	CO1	Observe the frequency response of single stage amplifier circuits by hardware implementation
	CO2	Observe the frequency response of multistage amplifier circuits by hardware implementation
	CO3	Implement and observe the working of low frequency and high frequency oscillators.
	CO4	Analyze the given differential amplifier circuit
	CO5	Realize the effect of negative feedback on circuit parameters.
	CO6	Examine the given circuit for the possible faults.

Exp. No.	Experiment Details	Ref.	Marks
1	Obtain bandwidth and gain for the given single stage CS amplifier	1,2	5
2	Obtain frequency response of cascode amplifier	1,2	5
3	Design RC phase shift oscillator for 1KHz frequency.	1,2	5
4	Obtain frequency of oscillation for Colpitt's and Hartley oscillator.	1,2	5
5	Analyze effect of negative feedback on gain and bandwidth for current series negative feedback amplifier.	1,2	5
6	Compare theoretical and observed differential gain, common mode gain and CMRR for the given differential amplifier circuit.	1,2	5
7	Identify the circuit and possible faults in it for the desired output for given hardware	1,2	5
8	Simulate Class A, Class B and Class AB power amplifier circuits using given simulation tool.	1,2	5
Total Marks			40



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References:

- [1] Datasheets of components
- [2] Donald A. Neamen, "Electronic Circuit Analysis and Design", TATA McGraw Hill, Second Edition.