

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
CPC601	System Programming and Compiler Construction	4	-	--	4	-	--	4
		Examination Scheme						
		ISE		MSE	ESE			
		10		30	100 (60% Weightage)			

Pre-requisite Course Codes	CPC502 (Operating System)
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At end of successful completion of this course, student will be able to

Course Outcomes	CO1	To understand the basics of system programs like editors, compiler, assembler, linker, loader, interpreter and debugger.
	CO2	To understand concepts of assemblers and write macros as and when required to increase readability and productivity.
	CO3	To understand the various phases of compiler and working of parsers.
	CO4	To understand role of Intermediate Code Generation in connection with language designing and apply optimization principles on given code.
	CO5	To analyze the role of Operating System functions such as memory management aspertaining to run time storage management.

Module No.	Topics	Ref.	Hrs.
1	System Software Concept, introduction to various system programs such as assemblers, loaders , linkers ,macro processors, compilers, interpreters, operating systems, device drivers	1-5	01
2	Assemblers General Design Procedure , Design of Assembler (Single Pass –Assembler IBM PC , multi pass Assembler - IBM 360/370 Processor),Statement of Problem , Data Structure , format of Databases , Algorithm, Look for modularity	1-5	06
3	Macros & Macro processors Macro instructions, Features of Macro facility, Design of 2 passmacroprocessor	1-5	04
4	Loaders and Linkers Loader schemes, Design of Absolute loader , Design of Direct linking Loader	1-5	04
5	Software Tools Software Tools for Program development, Editors: Types of	1-5	02



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	Editors, Design of Editor, Debug Monitors		
6	Compilers Introduction to Compilers, Phases of a compiler, comparison of compilers and interpreters.	1-5	02
7	Lexical Analysis Role of a Lexical analyzer, input buffering, specification and recognition of tokens, Designing a lexical analyzer generator, Pattern matching based on NFA's.	1-5	02
8	Syntax Analysis Role of Parser, Top down parsing, Recursive descent and predictive parsers (LL), Bottom Up parsing, Operator precedence parsing, LR, SLR and LALR parsers.	1-5	08
9	Syntax Directed Translation Syntax directed definitions, Inherited and Synthesized attributes, Evaluation order for SDDs, S attributed Definitions, L attributed Definitions	1-5	03
10	Intermediate Code Generation Intermediate languages: declarations, Assignment statements, Boolean expression, case statements, back patching, procedure calls.	1-5	04
11	Code Generation Issues in the design of Code Generator, Basic Blocks and Flow graphs, code generation algorithm, DAG representation of Basic Block	1-5	04
12	Code Optimization Principal sources of Optimization, Optimization of Basic Blocks, Loops in Flow graph, Peephole Optimization	1-5	03
13	Run Time storage Storage Organization, storage allocation strategies, parameter passing, Symbol table, introduction to garbage collection and compaction	1-5	04
14	Compiler-compilers JAVA compiler environment, YACC compiler-compiler	1-5	01
Total			48

References:



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- [1] J. J Donovan, "Systems Programming", Tata McGraw Hill Publishing Company
- [2] A. V. Aho, R. Shethi and J.D. Ulman, "Compilers - Principles, Techniques and Tools", Pearson Education
- [3] A. V. Aho, R. Shethi, Monica Lam, J.D. Ulman "Compilers - Principles, Techniques and Tools", Pearson Education, Second Edition.
- [4] D. M Dhamdhere, "Systems programming", Tata McGraw Hill
- [5] John R. Levine, Tony Mason & Doug Brown, "lex&yacc", O'Reilly, 2nd Edition