



# Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India  
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
EXL602	Digital Signal Processing and Processors Laboratory	--	--	2	--	--	1	1
		Examination Scheme						
		ISE		ESE			Total	
				Practical	Oral			
40	--	20		60				

Pre-requisite Course Codes		
After successful completion of the course, student will be able to		
Course Outcomes	CO1	Test Fast Fourier Transform Algorithm
	CO2	Design a Digital Filter
	CO3	Adapt open source tools for signal processing application
	CO4	Demonstrate Real Time Signal Processing using DSP Processor

Exp. No.	Experiment Details	Ref.	Marks
1	Convolution and Correlation		5
2	Discrete Fourier Transform		5
3	Fast Fourier Transform		5
4	Overlap Add Method / Overlap Save Method		5
5	Digital IIR Filter Design		5
6	Digital FIR Filter Design		5
7	Real Time Signal Processing		5
8	Signal Processing Application		5
<b>Total Marks</b>			<b>40</b>

### References:

As recommended by faculty.