



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Scheme for B.E./B.Tech Electronics Engineering (SEM V)					
SEM V					
Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits
		L	T	P	Total
EXC501	Microcontrollers and Applications	4	--	--	4
EXC502	Design with Linear Integrated Circuits	4	--	--	4
EXC503	Electromagnetic Engineering	4	--	--	4
EXC504	Signals and Systems	4	1	--	5
EXC505	Digital Communication	4	--	--	4
EXS506	Business Communication and Ethics	--	--	*4	2
EXL501	Microcontrollers and Applications Laboratory	--	--	2	1
EXL502	Design with Linear Integrated Circuits Laboratory	--	--	2	1
EXL503	Digital Communication Laboratory	--	--	2	1
EXL504	Mini Project I	--	--	2	2
Total		20	--	10+02	28

*Common to all branches

Scheme for B.E./B.Tech Electronics Engineering (SEM VI)					
SEM VI					
Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits
		L	T	P	Total
EXC601	Basic VLSI Design	4	--	--	4
EXC602	Advanced Instrumentation Systems	4	--	--	4
EXC603	Computer Organization	4	--	--	4
EXC604	Power Electronics I	4	--	--	4
EXC605	Digital Signal Processing and Processors	4	--	--	4
EXC606	Modern Information Technology for Management	2	--	--	2
EXL601	VLSI Design Laboratory	--	--	2	1
EXL602	Digital Signal Processing and Processors Laboratory	--	--	2	1
EXL603	Advanced Instrumentation and Power Electronics Laboratory	--	--	2	1
EXL604	Mini Project II	--	--	2	2
Total		22	--	8	27