

Con/5308-07.

(REVISED COURSE)

CD-5842

(3 Hours)

[Total Marks : 100

N.B. : (1) Question No. 1 is **compulsory**.

(2) Attempt any **four** questions from Q. Nos. 2 to 7.

(3) Assume **suitable data**, whenever **necessary**.

(4) Draw **neat diagram** whenever **necessary**.

1. (a) Explain Accumulator based computer with different block of it. 10
(b) Explain floating-point addition subtraction algorithm with flow chart. 10
2. (a) Explain virtual memory, why it is required, draw memory hierarchy. 10
(b) Explain TLB with it's advantages. 10
3. (a) Explain bus with bus arbitration methods. 10
(b) Explain memory interleaving techniques with different interleaving techniques. 10
4. (a) Explain different methods to design control unit of computer. differentiate between it. 10
(b) Differentiate between RISC and CISC processor. 10
5. (a) Explain different data transfer methods. 10
(b) Explain four-stages CPU instruction pipeline with diagram. 10
6. (a) Explain Cache-memory, with it's mapping scheme. 10
(b) Explain Flynn's classification scheme for parallel processor system. 10
7. Write short notes on any **two** : 20
 - (a) Page-Segmented Memory
 - (b) IEEE-754 Floating Point Format
 - (c) PCI Bus.