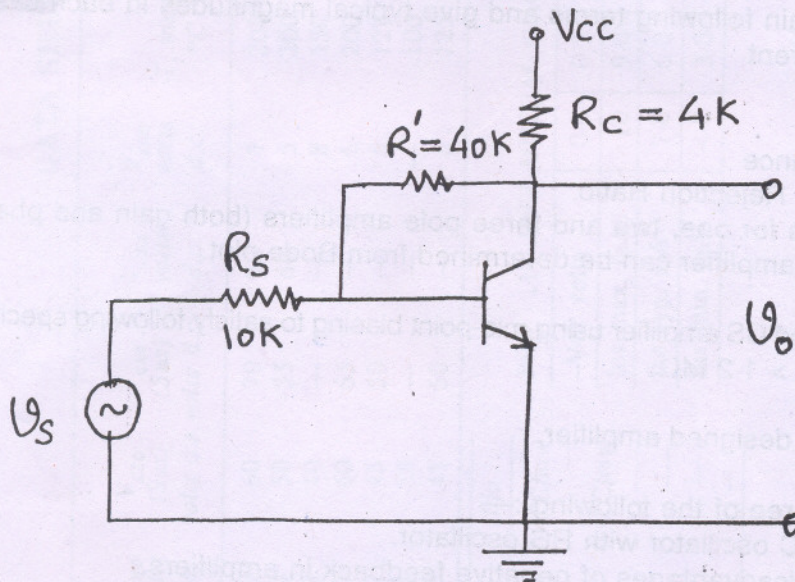


- N.B. (1) Question No. 1 is **compulsory**.
 (2) Attempt any **four** questions from Question Nos. 2 to 7.
 (3) Assume **suitable** data wherever **necessary**.
 (4) **Figures** to the **right** indicate **full marks**.

1. Attempt any **five** of the following :— 20
- State and prove Barkhuson's criteria for sustained oscillations.
 - Realise the following expression using practical OP-AMP :—

$$V_0 = 4V_3 - 2V_1 - 3V_2.$$
 - Explain the use of swamping resistor in differential amplifier.
 - Explain the advantages of class AB power amplifier over class B power amplifier with suitable waveforms.
 - Which type of feedback will you use to obtain amplifier with stable transconductance ? Draw one circuit diagram of such an amplifier.
 - Write a short note on Cascade amplifier.
2. (a) Design a class A power amplifier to provide 2 W power to the speaker of 4 Ω . 12
 (b) Design a suitable heat sink using transistor 2N3055 for following application :— 8
 Actual power dissipation in transistor = 40 Watts,
 Maximum thermal resistance from case to heat sink $\theta_{C-HS} = 0.5^\circ\text{C/W}$,
 Ambient temperature = 40°C.
 (Refer data sheet for transistor data.)
3. (a) Draw the circuit diagram of Colpitts Oscillator and explain its working. Derive expression for frequency of sustained oscillations. Determine condition to be satisfied for sustained oscillations. 10
 (b) For the circuit shown in **figure**, determine A_{vf} , R_{if} and R_{of}' using negative feedback approach. 10



Assume —

$$h_{ie} = 1.1 \text{ K}\Omega,$$

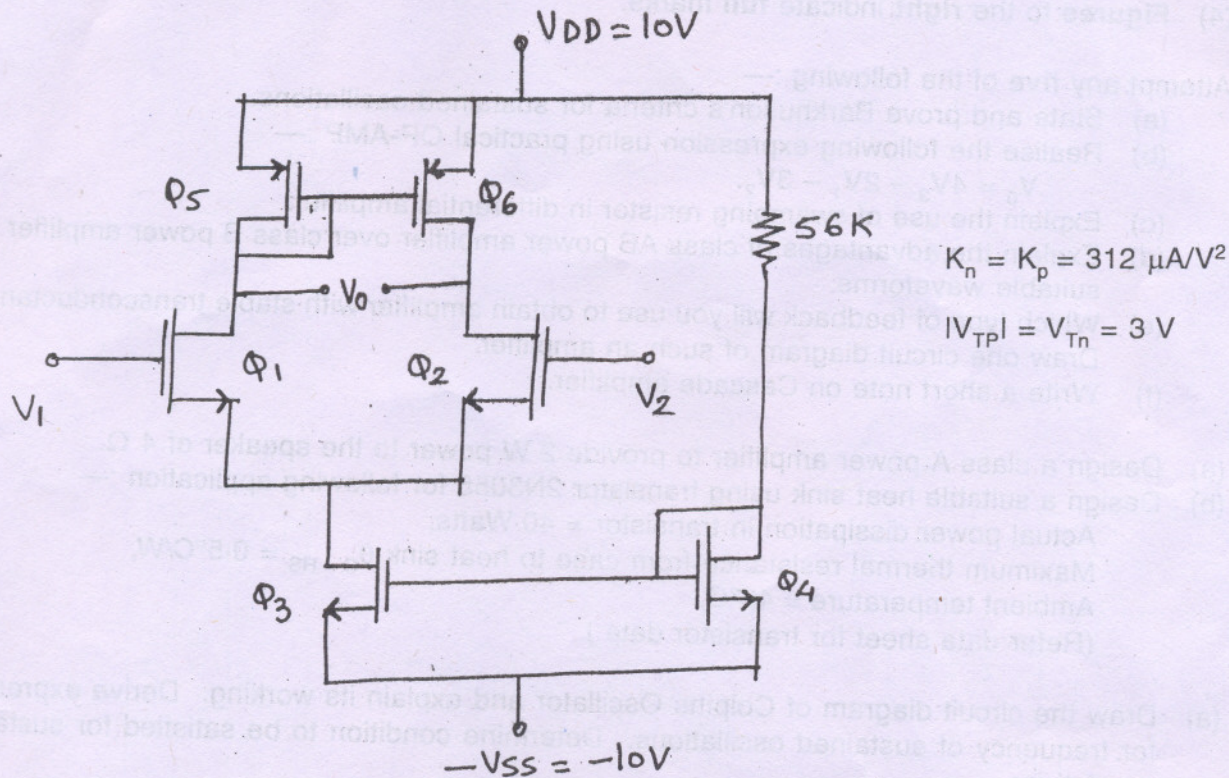
$$h_{fe} = 50,$$

$$h_{re} = h_{oe} = 0$$

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4. Determine and derive expressions for differential mode gain, common mode gain and CMRR for the circuit shown in **figure**. Assume early voltage $|V_A| = 100 \text{ V}$ for n-MOSFET and $|V_A| = 50 \text{ V}$ for P-MOSFET, for calculations of drain resistance of a transistor. Find the Q point of main transistors Q_1 and Q_2 and draw the frequency response nature. **20**



5. (a) For OP-AMP 741, explain following terms and give typical magnitudes in each case :— **10**
- Input bias current
 - CMRR
 - Slew rate
 - Output resistance
 - Power Supply Rejection Ratio.
- (b) Draw typical Bode plots for one, two and three pole amplifiers (both gain and phase) and explain how stability of amplifier can be determined from Bode plot. **10**
6. Design a two stage RC coupled CS amplifier using mid-point biasing to satisfy following specifications. **20**
- $|A_V| \geq 15$, $V_o = 3 \text{ V}$, $R_i > 1.2 \text{ M}\Omega$.
- Use JFET BFW11.
- Determine A_V , R_i and R_o for designed amplifier.
7. Write short notes on any **three** of the following :— **20**
- Compression of LC oscillator with RC oscillator.
 - Advantages and disadvantages of negative feedback in amplifiers.
 - Nyquist stability criteria.
 - Concept of virtual ground in OP-AMP.

DATA SHEET

Transistor type	P_{dmax}	I_{cmax}	$V_{CE}^{(sat)}$	V_{CBO}	V_{CEO}	V_{CER}	V_{CEX}	V_{BEO}	T_j max	D.C.	current	gain	Small	Signal	h_{fe}	V_{BE}	θ_{fe}	Derate					
	@ 25°C	@ 25°C	volts	volts	(SUS)	(SUS)	volts	volts											Watts	Amps	d.c.	d.c.	volts d.c.
2N 3055	115.5	15.0	1.1	100	60	70	90	7	200	20	50	70	15	50	120	1.8	1.5	0.7					
ECN 055	50.0	5.0	1.0	60	50	55	60	5	200	25	50	100	25	75	125	1.5	3.5	0.4					
ECN 149	30.0	4.0	1.0	50	40	—	—	8	150	30	50	110	33	60	115	1.2	4.0	0.3					
ECN 100	5.0	0.7	0.6	70	60	65	—	6	200	50	90	280	50	90	280	0.9	35	0.05					
BC147A	0.25	0.1	0.25	50	45	50	—	6	125	115	180	220	125	220	260	0.9	—	—					
2N 525(PNP)	0.225	0.5	0.25	85	30	—	—	—	100	35	—	65	—	45	—	—	—	—					
BC147B	0.25	0.1	0.25	50	45	50	—	6	125	200	290	450	240	330	500	0.9	—	—					

Transistor type	h_{ie}	h_{oe}	h_{re}	θ_{ja}
BC 147A	2.7 K Ω	18 $\mu \text{ S}$	1.5×10^{-4}	0.4°C/mw
2N 525 (PNP)	1.4 K Ω	25 $\mu \text{ S}$	3.2×10^{-4}	—
BC 147B	4.5 K Ω	30 $\mu \text{ S}$	2×10^{-4}	0.4°C/mw
ECN 100	50 Ω	—	—	—
ECN 149	15 Ω	—	—	—
ECN 055	12 Ω	—	—	—
2N 3055	6 Ω	—	—	—

BFW 11—JFET MUTUAL CHARACTERISTICS

-V _{Gs} volts	0.0	0.2	0.4	0.6	0.8	1.0	1.2	1.6	2.0	2.4	2.5	3.0	3.5	4.0
I _{DS} max. mA	10	9.0	8.3	7.6	6.8	6.1	5.4	4.2	3.1	2.2	2.0	1.1	0.5	0.0
I _{DS} typ. mA	7.0	6.0	5.4	4.6	4.0	3.3	2.7	1.7	0.8	0.2	0.0	0.0	0.0	0.0
I _{DS} min. mA	4.0	3.0	2.2	1.6	1.0	0.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

N-Channel JFET

Type	V_{DS} max.	V_{DG} max.	V_{GS} max.	P_d max.	T_j max.	I_{DSS}	g_{mo}	$-V_p$ Volts	r_d	Derate	θ_{ja}
	Volts	Volts	Volts	@25°C			(typical)			above 25°C	
2N3822	50	50	50	300 mW	175°C	2 mA	3000 $\mu \text{ S}$	6	50 K Ω	2 mW/°C	0.59°C/mW
BFW 11 (typical)	30	30	30	300 mW	200°C	7 mA	5600 $\mu \text{ S}$	2.5	50 K Ω	—	0.59°C/mW