

Con. 5910-08.

RC-8742

(REVISED COURSE)

(3 Hours)

[Total Marks : 100

N.B. : (1) Question No.1 is **compulsory**.

(2) Solve any **four** questions out of **remaining**.

(3) Draw **neat labelled diagram** wherever **necessary**.

1. (a) Explain with suitable examples the difference between computer architecture and computer organization. 5
- (b) Prove that Memory Access Time (MAT) is less than that of the memory cycle time (MCT) with suitable example. 5
- (c) What is bit-slice ALU ? Explain in brief the structure of AMD 2901 IC and design 12 bit ALU using AMD 2901 IC. 10
2. (a) Explain and solve the following problem using by non-restoring division algorithm. Hence divide $(10)_{10}$ with $(3)_{10}$. 10
- (b) A two level memory (M1 , M2) has the access times $tA1 = 10^{-8}$ s and $tA2 = 10^{-3}$ s. What must be the hit ratio H in order for the access efficiency to be atleast 65 % of it's maximum possible value ? 10
3. (a) A block set associative cache consists of a total of 128 cache block with two blocks/set. The main memory containing 4 K blocks with 16 words/ block. Draw a figure explaining the mapping and show the partitions of an address into TAG, SET, and WORD bit. What is cache coherency problem ? 10
- (b) Explain with state diagram, MESI (Mutual Exclusive Shared Invalid) protocol. 10
4. (a) Explain the general organization of CPU. State the function of following CPU registers :— 10
 - (i) MAR (Memory Address Register)
 - (ii) MDR (Memory Data Register)
 - (iii) IR (Instruction Register)
 - (iv) PC (Program Counter)
 - (v) SP (Stack Pointer).
- (b) Define "(Input/Output) I/O Module". State the difference between programmable and non-programmable device. Explain in brief the structure of 8089 I/O processor. 10

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5. (a) Define the term "softwired" and "hardwired". Explain with diagram the softwired i.e. micro programmed and hardwired control unit organization. **10**
- (b) What is "Micro program"? Write a Micro program using RTL (register transfer language) notation for the following arithmetic operation :— **10**
- (i) ADD R1,R2 i.e $R1 \leftarrow R1 + R2$
 - (ii) MUL R1,R2 i.e $R1 \leftarrow R1 * R2$
6. (a) Explain in brief about SP ARC processor. Draw and explain in brief 3-bit windows architecture of SP ARC processor. **10**
- (b) What is the difference between pipelining & parallelism? Explain the Flynn's Classification in detail. **10**
7. (a) What is pipelining? Explain with suitable example the difference between serial instruction execution and pipelined instruction execution. Also prove that why pipelined instruction execution is faster as compared to serial instruction execution. **10**
- (b) Explain systolic processor with suitable example. **10**