9/12/09 ME | ETRX | sem II 10:30 40 2230 micro-and system I 40: 2ndExm.09-AM(a) **BB-6158** Con. 5119-09. Microprocessors & System II (3 Hours) [Total Marks: 100 Ty N.B.: (1) Question No. 1 is compulsory. Attempt any four questions out of remaining questions. State any additional data assumed, if necessary to answer. 1. (a) With suitable diagrams explain superscalar nature and pipe line features for Pentium. 10 (b) Giving timing diagram, explain when and how the burst bus cycles are conducted 10 by Pentium. (a) Explain reflected wave switching and its use in PCI bus. Why bus parking is needed? 10 How it is done? (b) Explain how interrupts are handled in PCI bus. 10 (a) Describe the data and code cache structures in detail for Pentium. 10 (b) Explain cache control signals of Pentium. How they can be used to maintain cache 10 coherency in the Pentium based machine? 4. (a) Explain with suitable diagrams how data transactions occur on the USB bus. 10 (b) Explain how bus arbitration is achieved in PCI bus. What is hidden bus arbitration? 10 (a) Describe the requirements for a real time operating system. 10 (b) Explain the functions of microkernel for QNX operating system. 10 (a) Explain the functions of following PCI signals. 10 (i) FRAME (ii) IRDY (iii) DEVSEL (iv) SDONE (v) SBO (b) Explain bus access Latency for PCI bus. Explain the function of Latency Timer. 10

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7. Write notes on any two of the following: -

(b) Special bus cycles for Pentium

(c) Branch prediction logic of Pentium

(d) System management mode in Pentium.

(a) Fire Wire bus