

Lib

Electronic Circuit Analysis & Design

N.B. : (1) Question No. 1 is compulsory.

(2) Attempt any four questions from question Nos. 2 to 7.

2.30 to 5.30

(3) Assume suitable data wherever necessary with proper justification.

(4) Figure to the right indicate full marks.

1. Attempt any four of the following :-

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(a) Design a circuit to have sine wave with  $f = 2\text{kHz}$  without giving any input signal.

(b) State the methods to improve CMRR of an Differential Amplifier. Explain any one.

(c) Three non-identical amplifiers are cascaded together with following parameters,

$A_{v1} = 40 \text{ db}, Bw_1 = 40 \text{ Hz to } 100 \text{ kHz}$

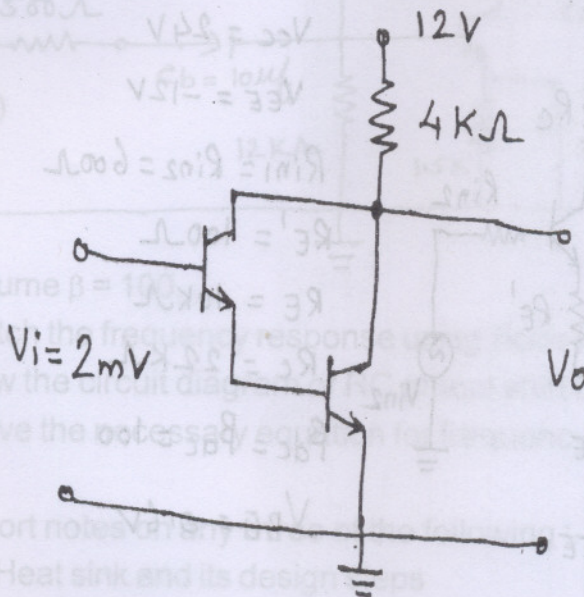
$A_{v2} = 42 \text{ db}, Bw_2 = 35 \text{ Hz to } 200 \text{ kHz}$

$A_{v3} = 38 \text{ db}, Bw_3 = 45 \text{ Hz to } 150 \text{ kHz.}$

Determine the voltage gain and Bandwidth of the Cascaded Amplifier.

(d) Explain the advantages of class AB amplifier over class B amplifier with suitable waveforms.

(e) Determine output voltage for the following circuit,

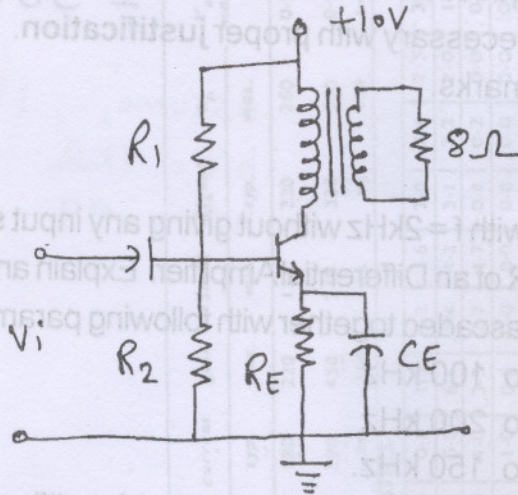


Assume,  
 $h_{fe} = 100,$   
 $h_{ie} = 1.2 \text{ k}\Omega$

2. Design a two stage RC coupled cascaded amplifier for the following requirements : 20

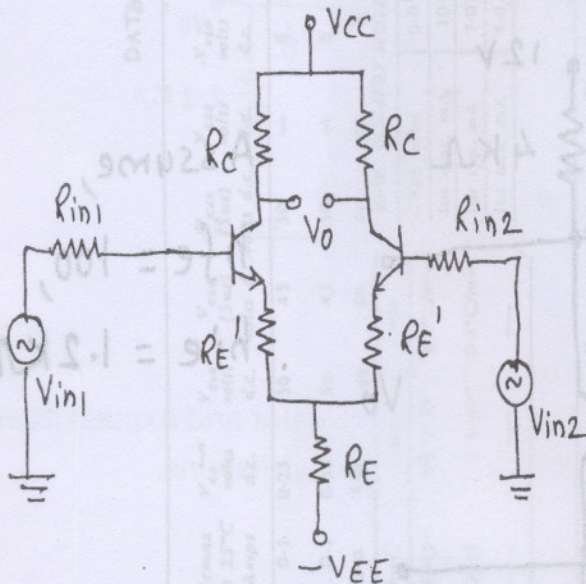
$A_v \geq 1000, V_o = 3 V_{\text{peak}}, R_i > 100 \text{ k}\Omega, F_L$  better than 20 Hz.

3. (a) Design a class A power amplifier to provide 3W power to a speaker of  $4\ \Omega$ . 12  
 (b) Calculate the ac power delivered to the  $8\ \Omega$  speaker, dc input power, power 8  
 dissipated by the transistor and efficiency of the circuit shown in **figure**.



$V_{CEQ} = 10V$   
 $I_{CEQ} = 140mA$   
 $V_{CEmin} = 1.7V$   
 $V_{CEmax} = 18.3V$   
 $I_{Cmin} = 25mA$   
 $I_{Cmax} = 255mA$

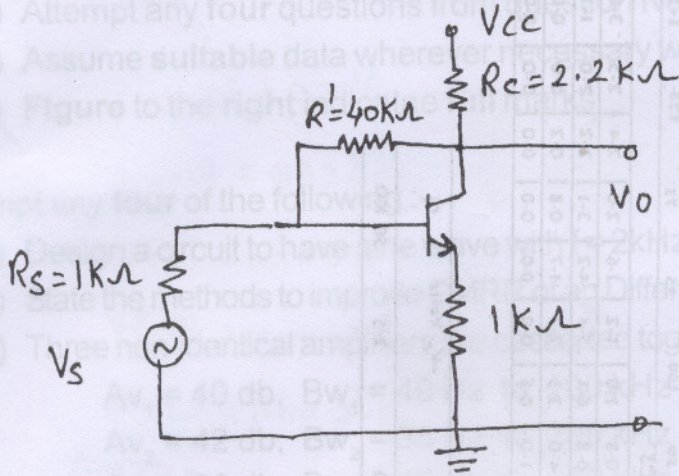
- 4 (a) For the circuit shown in **figure**, determine the quiescent current  $I_{CQ}$ , voltage  $V_{CEQ}$  and differential gain  $A_d$ . 10



$V_{CC} = 24V$   
 $V_{EE} = -12V$   
 $R_{in1} = R_{in2} = 600\ \Omega$   
 $R_{E'} = 100\ \Omega$   
 $R_E = 10K\ \Omega$   
 $R_C = 22K\ \Omega$   
 $\beta_{DC} = \beta_{AC} = 100$   
 $V_{BE} = 0.6V$

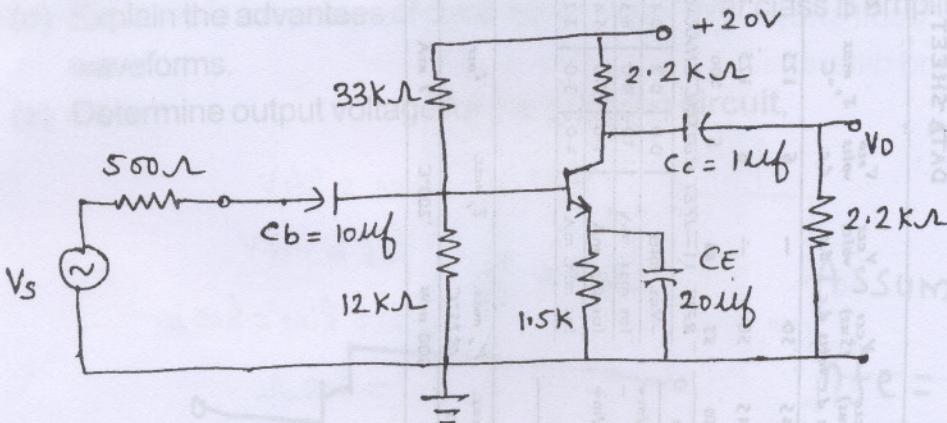
- (b) Explain the high frequency analysis of a BJT amplifier. Derive necessary 10  
 expressions and show the two pole Bode Plot of the same.

5. (a) State the advantages of negative feed back on performance of amplifier. 10  
 Compare various types of negative feed back.
- (b) For the circuit shown in **figure**, determine  $A_{v_f}$ ,  $A_{i_f}$ ,  $R_{i_f}$  and  $R_{o_f}$ . 10



Assume,  
 $h_{ie} = 1k\Omega$   
 $h_{fe} = 99$

6. (a) Determine the lower cutoff frequency of the network of **figure** below : 10



Assume  $\beta = 100$ .

Sketch the frequency response using Bode Plot.

- (b) Draw the circuit diagram of RC phase shift oscillator and explain its working. 10  
 Derive the necessary equation for frequency of oscillations.

7. Write short notes on any **three** of the following :- 20
- (a) Heat sink and its design steps
  - (b) Nyquist stability criteria to estimate stability of an amplifier
  - (c) Crystal oscillator
  - (d) Comparison of small signal and large signal amplifiers
  - (e) Differential amplifier with active load.

## DATA SHEET

Transistor type	$P_{dmax}$	$I_{cmax}$	$V_{ce}^{1-0}$	$V_{ceo}$	$V_{ceo}$	$V_{ces}$	$V_{ces}$	$V_{sio}$	$T_j$	D.C. current	gain	Small Signal		$h_f$	$V_{ce}$	
	@ 25°C Watts	@ 25°C Amps	volts d.c.	volts d.c.	(Sus) volts d.c.	(Sus) volts d.c.	volts d.c.	°C	min			typ.	max.			min.
BC147A	0.25	0.1	0.25	50	45	50	—	6	125	115	180	220	125	220	260	0.9
BC147B	0.25	0.1	0.25	50	45	50	—	6	125	200	290	450	240	330	500	0.9
ECN055	30.0	5.0	1.0	60	50	55	60	5	200	25	50	100	25	75	125	1.5

Transistor type	$h_{ic}$	$h_{oc}$	$h_{re}$	$\alpha_{ja}$
BC 147A	2.7 K $\Omega$	38 $\mu\Omega$	$1.5 \times 10^{-4}$	0.4°C/mw
BC 147B	4.5 K $\Omega$	30 $\mu\Omega$	$2 \times 10^{-4}$	0.4°C/mw
ECN055	30 $\Omega$	—	—	—

## BFW 11 JFET MUTUAL CHARACTERISTICS

$-V_{GS}$ volts	0.0	0.2	0.4	0.6	0.8	1.0	1.2	1.6	2.0	2.4	2.5	3.0	3.5	2.0
$I_{OS}$ max. mA	10.0	9.0	8.3	7.6	6.8	6.1	5.4	4.2	3.1	2.2	2.0	1.1	0.5	0.0
$I_{OS}$ typ. mA	7.0	6.0	5.4	4.6	4.0	3.3	2.7	1.7	0.8	0.2	0.0	0.0	0.0	0.0
$I_{OS}$ min. mA	4.0	3.0	2.2	1.6	1.0	0.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

## N-Channel JFET

Type	$V_{DS}$ max. Volts	$V_{DC}$ max. Volts	$V_{GS}$ max. Volts	$P_d$ max. @25°C	$T_j$ max.	$I_{DSS}$	$r_{ds}$	$-V_p$ Volts	$r_d$
BFW 11 (typical)	30	30	30	300 mW	200°C	7 mA	5000 $\mu\Omega$	2.5	50 K $\Omega$