

T.E / V | Rev. (Extc) May'2007 / C.A.O / U.T.EXAM.

VT Apr'07 152

Con/2315-07.

(REVISED COURSE)
(3 Hours)

ND-8318
[Total Marks : 100

N.B. : (1) Question No. 1 is **compulsory**.

(2) Attempt any **four** questions out of remaining **six** questions.

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| 1. | (a) Explain Booth's multiplication algorithm with the help of flow chart. | 10 |
| | (b) Explain RISC and CISC characteristics. | 10 |
| 2. | (a) Explain microprogram sequencing with suitable example. | 10 |
| | (b) Explain Flynn's classification of parallel processing systems. | 10 |
| 3. | (a) List different Cache memory mapping techniques and explain any one technique. | 10 |
| | (b) (i) Distinguish SRAM and DRAM. | 10 |
| | (ii) Explain I/O instructions. | 10 |
| 4. | (a) Distinguish Hardwired and microprogrammed control unit. | 10 |
| | (b) Write detailed note on I/O processor. | 10 |
| 5. | (a) Explain virtual memory address translation. | 10 |
| | (b) Explain control unit design for gcd processor. | 10 |
| 6. | (a) Explain steps involved in design of ALU. | 10 |
| | (b) Draw and explain the four stage floating point adder pipeline. | 10 |
| 7. | Write notes on any two : | 20 |
| | (a) RAID | |
| | (b) Generation of Computers | |
| | (c) Programmed I/O and Interrupt driven I/O. | |
