

S.E. Sem 4 (Rev.)
Comp & I.T.

Computer Organization & Architecture

27/5/08

ws April 08 460

Con. 3093-08.

(REVISED COURSE)

CO-9760

(3 Hours)

[Total Marks : 100

- Note: (1) Question 1 is compulsory.
 (2) Attempt 4 questions from remaining.
 (3) Assume suitable data is required.
 (4) Draw neat diagram wherever necessary.

1.	A)	Explain different addressing modes with examples.	10
	B)	Explain Programmed I/O & interrupt driven I/O.	10
2.	A)	Discuss different memory interleaving techniques.	10
	B)	Discuss the properties of RISC & CISC processors.	10
3.	A)	Explain the RAID levels used for storage.	10
	B)	Explain the hardwired control unit with the help of sequence counter method & delay element method.	10
4.	A)	Explain different mapping techniques of Cache memory.	10
	B)	Explain Booth's algorithm with flowchart & example.	10
5.	A)	Explain Wilke's micro programmed control unit.	10
	B)	Differentiate between SRAM & DRAM.	10
6.	A)	Explain page replacement algorithms.	10
	B)	Explain PCI Bus.	10
7.	Write short note on (Any <u>3</u>) :-		20

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| 1) Demand paging | 2) Locality of reference |
| 3) Floating point ALU | 4) Virtual memory. |