

T.E. Sem 5 (REV.)

VT-May, 08

EXTC

Computer Architecture &
Organisation
(REVISED COURSE)

24/5/08

Con. 2956-08.

CO-2557

(3 Hours)

[Total Marks : 100

N.B. : (1) Question No. 1 is **compulsory**.

(2) Attempt any **four** questions out of remaining **six** questions.

1. (a) Explain Booth's algorithm for multiplication of signed numbers. 10
(b) Compare RISC and CISC computers. 10
2. (a) List the different mapping techniques of Cache memory and explain any one out of those. 10
(b) What is bus arbitration ? Explain any one method of bus arbitration. 10
3. (a) Distinguish hardwired control unit and microprogrammed control unit. 10
(b) Write note on RAID. 10
4. (a) Distinguish memory mapped I/O and I/O mapped I/O. 10
(b) Explain Flynn's classification of digital computers. 10
5. (a) Explain interrupt driven I/O. 10
(b) Explain pipeline concept and the stages of pipelined computer. 10
6. (a) What do you mean by addressing mode ? Explain addressing modes. 10
(b) With block diagram explain accumulator based CPU. 10
7. Write notes on :- (any **two**) 20
 - (a) Computer system design levels
 - (b) IOP and CPU interaction
 - (c) DMA operation.