S.E. EXTC CIII) (0/d).
EDC-I.

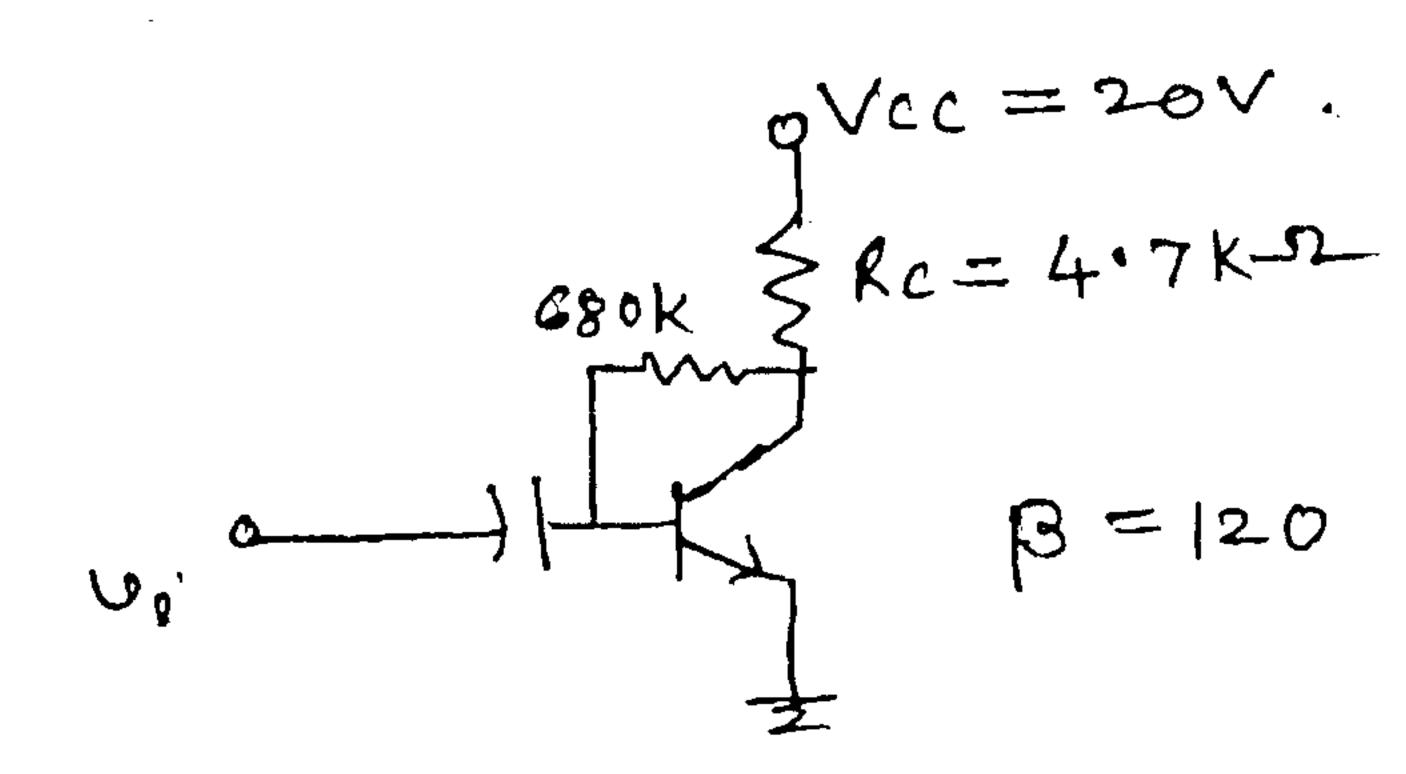
(OLD COURSE) Q.P. Code: 4679

(3 Hours) [Total Marks: 100

- N.B.: (1) Question Nos. 1 is compulsory.
 - (2) Attempt any four questions from the remaining six questions.
 - (3) Figures to the right indicate full marks.
 - (4) Assume suitable data whenever necessary but justify the same.
 - 1. Design a single stage CE amplifier suitable for low frequencies upto 10Hz, to give a voltage gain |AV| ≥ 80 and output voltage of 4.5V employing transistor BC147A. Calculate the expected |AV| and maximum output voltage that can be obtained from circuit. Also calculate input resistance of the circuit specify clearly the supply voltage Vcc. Select stability factor S ≤ 10.
 - (a) Design a single stage common source amplifier for audio frequency applications suitable for operation upto low frequency of 20Hz. Use JFET type BFW-11 to give output voltage of 2V. and voltage gain |AV| = 10
 For design use mutual characteristics of VGS IDS(typ) given in data sheet.

Design for
$$ID = \frac{IDSS}{2}$$

- (b) Calculate
 - (i) input impedance
 - (ii) output impedance
 - (iii) voltage gain for the designed circuit.
- 3. (a) Explain the operation of transistor series regulator with one transistor, derive 10 expression for line & load regulation for the same.
 - (b) Draw output characteristics of common emitter configuration. Show how transistor amplifies a time varying signal by drawing a DC load line on the characteristics.
- 4. (a) For the circuit shown in figure Determine following.
 - (i) IC
 - (ii) VCE
 - (iii) VB
 - (iv) VC

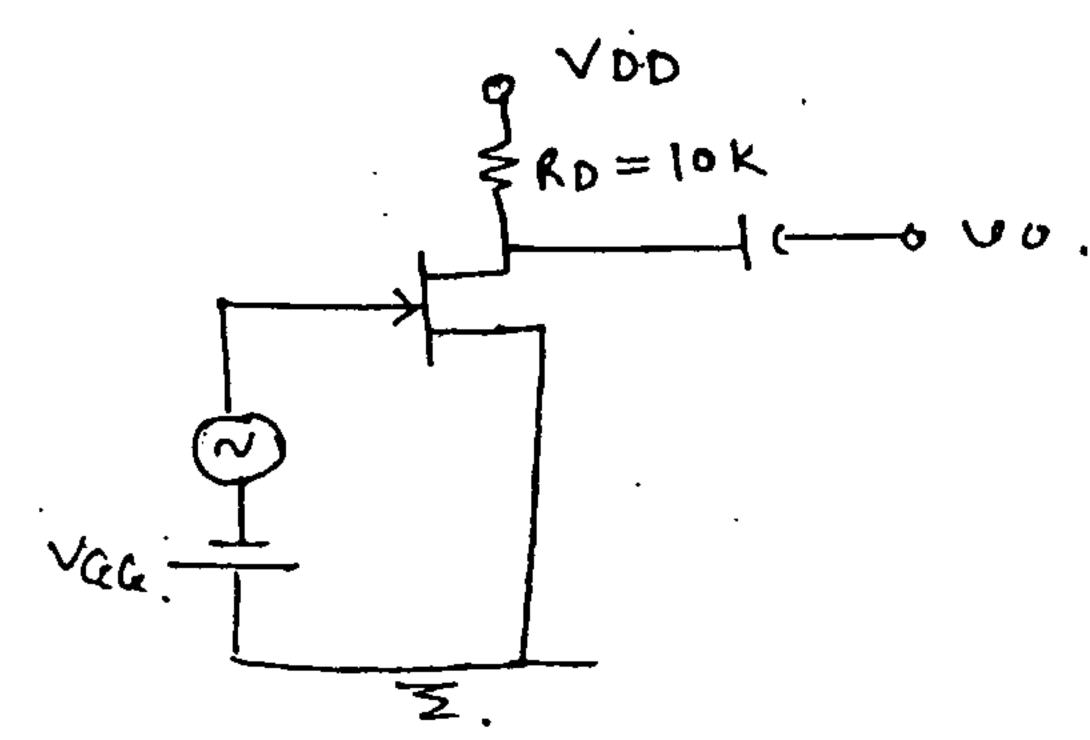


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(b) Calculate the voltage gain and output resistance of the following circuit. Given that gm = 2mA/v & rd = 50k.



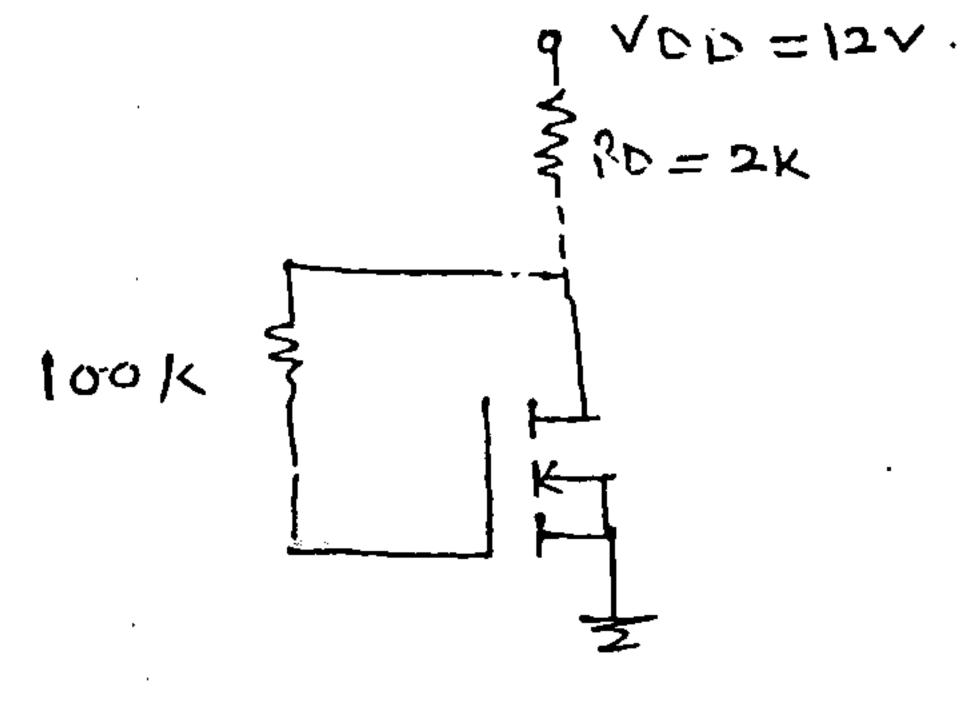
5. (a) Draw circuit diagram for half wave rectifier with capacitor filter with load resistor RL.

Explain the working by drawing appropriate waveforms derive the expression

for the ripple factor 'r'.

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- (b) The following parameters are obtained from certain JFET data sheet VGs off = -8V & IDss = 6mA. Determine the values of ID for each value of VGS ranging from 0V to 8V in 1V steps, plot the transfer characteristics for same data.
- 6. (a) ID (ON) = 6mA VGS (ON) = 8V VGS (Th) = 3V Determine
 - termine (i) IDQ
 - (ii) VDSQ



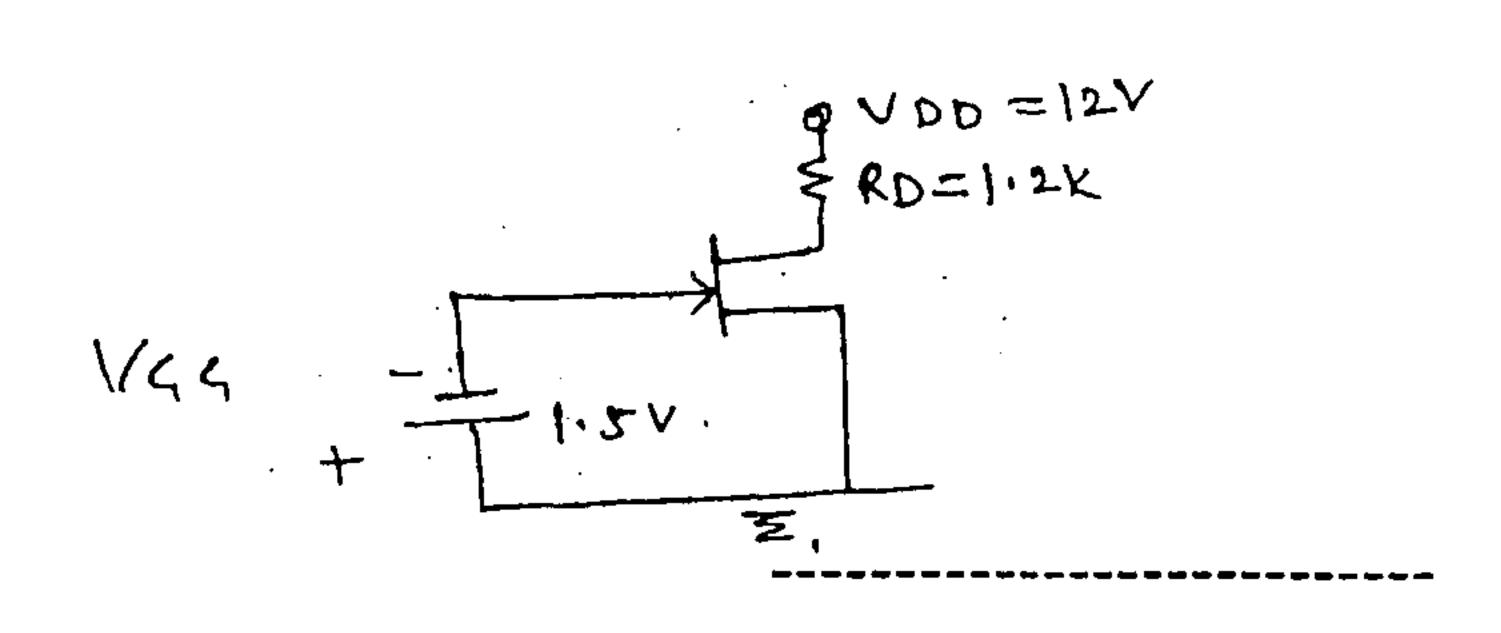
- (b) Explain any two
 - (i) UJT construction, characteristics & parameters.
 - (ii) SCR working & applications.
 - (iii) BJT as a switch
 - (iv)Power MOSFET
- 7. (a) Explain the working of UJT relaxation oscillator and draw waveform.

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(b) Determine drain current ID & VDS for fixed bias JFET cicuit.

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IDSS - 12mAVp = -4v

(Notes)	BEW 11	270822	. =	7770	N-Charm,	2N 3055,	ECN 085	ECN 149	ECH 100	BC 147B	2N 525 (PI	BC 147 A	Transistor	BC 147 B	PNP)	2N 525	8C 147A		ECN 149	ECN 055	2N 3055		•	3	Transistor
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QP Code: 4683

(OLD COURSE)

(3 Hours)

[Total Marks: 100

- N.B. (1) Question No. 1 is compulsory.
 - (2) Solve any four out of remaining six questions.
 - (3) Each question carries 20 marks. Equal marks for the subquestions.
 - (4) Assume suitable data if required.
- 1. (a) Perform the following operations
 - (i) (1101.0), x (110.1),
 - (ii) $(57)_{R} (47)_{R}$
 - (iii) $(75)_{10} (55)_{10}$ using 2's complement method.
 - (iv) $(111010 \cdot 110)_2 \div (1010)_2$
 - (i) Explain minterm and Maxterm (b)
 - (ii) Justify, NAND & NOR gates are Universal gates.
 - (iii) Find M if $(193)_{M} = (623)_{8}$
 - (iv) Differentiate between synchronous and Asynchronous counter.
- 2. (a) Prove the following using boolean algebraic theorems

(i)
$$\overline{(\overline{AB} + \overline{A} + AB)} = 0$$

(ii)
$$AB + \overline{AC} + \overline{ABC} (AB + C) = 1$$

- (b) State and prove DeMorgan's theorems.
- 3. (a) Simplity the following boolean function by using Quine Mc Cluskey method.

$$F(A,B,C,D) = \Sigma m (0, 2, 3, 6, 7, 8, 10, 12, 13)$$

Minimize the following logical equation using K-map and design the minimized equation using logic gates.

$$F(A,B,C,D) = \Pi M (0, 2, 3, 8, 9, 12, 13, 15)$$

- (a) Design the logic ckt for 1-bit comparator using NAND gates only.
 - (b) Draw and explain the working of clocked S-R Flip Flop with preset and clear using NAND gates only.

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- 5. (a) Design the logic ckt for Mod-6 Ripple counter using MS-JK FFs.
 - (b) Design the logic ckt for 3-bit SIPO Register using MS-D-FFs.
- 6. (a) Design the logic ckt for Full Subtractor using 3:8 Decoder.
 - (b) Design and implement the given logical equation using 16:1 Multiplexer.

$$Y = \Sigma m (1, 3, 4, 8, 10, 11, 12, 14, 15)$$

- 7. (a) Explain TTL and ECL Logic Families.
 - (b) Explain PAL and PLA.

QP Code: 4559

(OLD COURSE)

(3 Hours)

Total Marks: 100

- Question No. 1 is compulsory.
 - Attempt any four out of remaining six questions.
 - Make suitable assumptions if required and justify the same.
 - Find absolute, relative and percentage error in following numbers. Determine number of significant digits.

i) a = 123.41769543

 $\bar{a} = 123.41$

ii) b = 0.0053102500

 $\overline{b} = 0.0051$

iii) c = 450550

 $\bar{c} = 450552$

Define the operators $\Delta, \nabla, \delta, \mu \& E$. Prove that (b)

i) $2\mu\delta = \Delta + \nabla$

ii) $E = 1 + \Delta$

Using Picard's method solve

 $\frac{dy}{dx} = 1 + xy \quad \text{such that } y = 0 \text{ when } x = 0.$

(d) Derive the equation for Regula – falsi method using geometrical interpretation.

- List the bracketing methods and oven methods and find the real root of the equation $x^3 - 4x - 9 = 0$ using Newton Raphson method correct to three decimal places.
 - 10
 - Solve the following equations by Gauss Seidel method. 27x+6y-z=85, 6x+15y+2z=72, x+y+54z=110.
- From the following table find the number of students who obtained marks less than 45.

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- 60-70 30-40 40-50 50-60 Marks 35 51 No. of students 42 31
- Using Newton's divided difference formula, find the value of f(9) from 10 the following table.

 \boldsymbol{x} 5202 392 1452 2366 150

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4. (a) Write a program for Lagrange's interpolation method and using this formula, find the value of y when x = 10 from the following table.

\boldsymbol{x}	5	6	9	11
y	12	13	14	16

(b) The result of measurement of electric resistance R of a copper bar at various temperatures t^0C are listed below:

			_		_				
	t	19	25	30	36	40	45	50	10
i	R	76	77	79	80	82	83	85	

Find a relation R = a + bt

5. (a) The velocity of the train which starts from rest is given by the following 10 table, the time being reckoned in minutes from the start and speed in km/hour.

Time	3	6	9	12	15	18
Velocity	22	29	31	20		0

Estimate approximately the distance covered in 18 ininutes by Simpson's 3/8th rule.

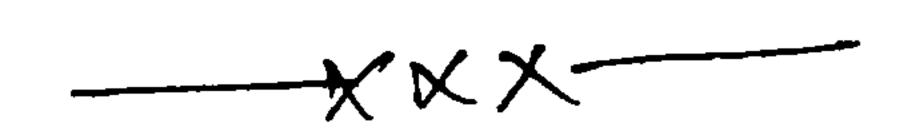
- Solve $\frac{dy}{dx} = x + y^2$ with $x_0 = 0$, $y_0 = i$ by Euler's modified formula find the value of y when x = 0.5 taking h = 0.25.
- 6. (a) Solve $\frac{dy}{dx} = x + y$ with initial conditions y(1) = 2 and find y at x = 1.2, x = 1.4 by Runge Kutta Method of Fourth Order taking h = 0.2.
 - (b) Solve the following set of equations using Gauss Elimination method. 10 2x + y + z = 10, 3x + 2y + 3z = 18, x + 4y + 9z = 16.
- 7. (a) Explain the propagation of errors.

Using Adams – Bashforth method, obtain the solution of $\frac{dy}{dx} = x - y^2$ at y(0.8), given values

x	0	0.2	0.4	0.6
y	0	0.0200	0.0795	0.1762

(c) Write a short note on Golden section search.

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(OLD COURSE)

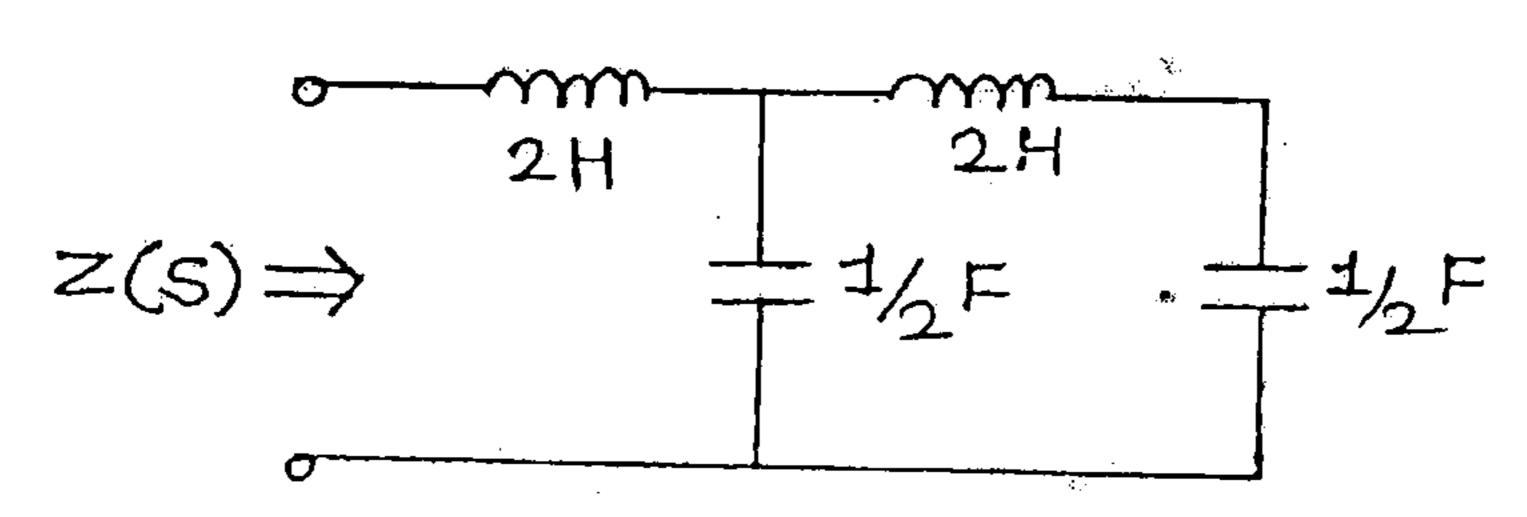
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(3 Hours)

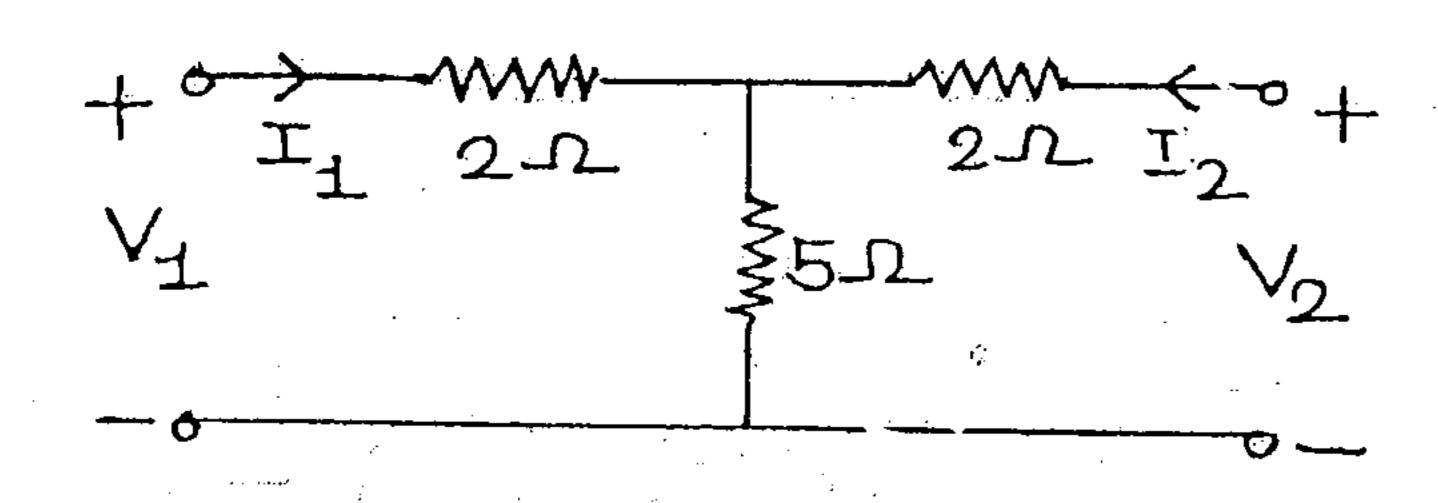
[Total Marks:100]

N.B.: (1) Question No. 1 is compulsory.

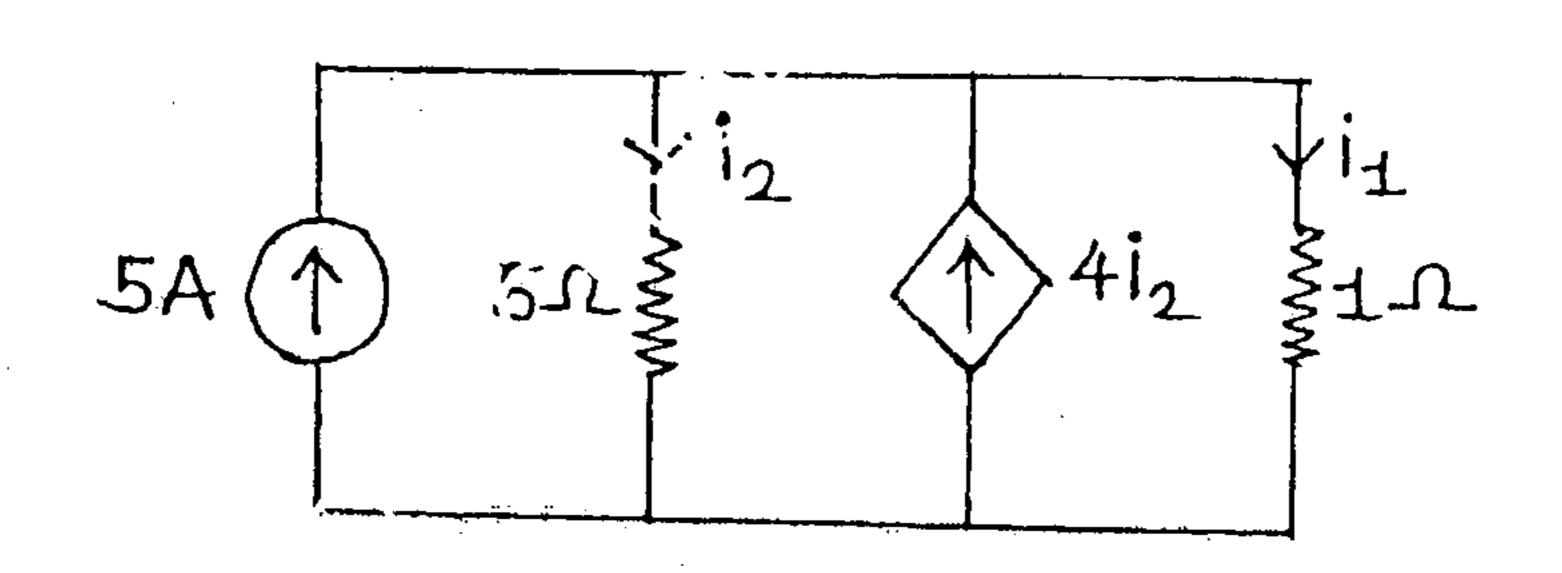
- (2) Attempt any four from the remaining questions.
- (3) Assume suitable data, if required.
- 1.(a) Find the driving point impedance of network.



For the given network find out z parameters and verify the condition of reciprocity.



Find current il and i2 in the given circuit.

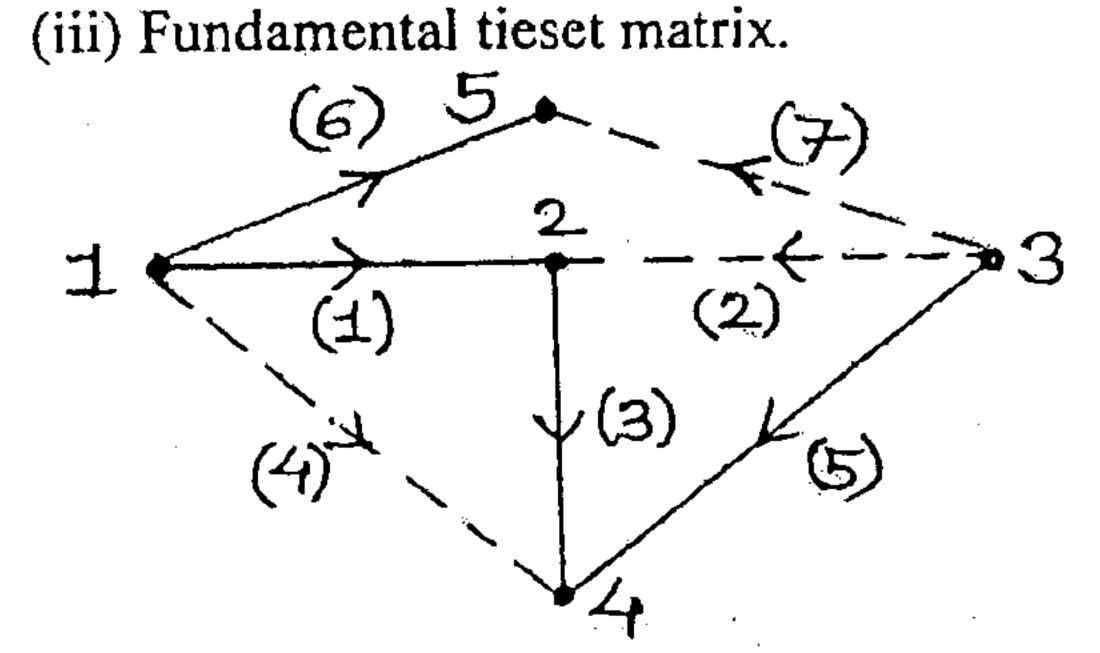


Check for Hurwitz $P(s) = s^4 + 3s^2 + 2$.

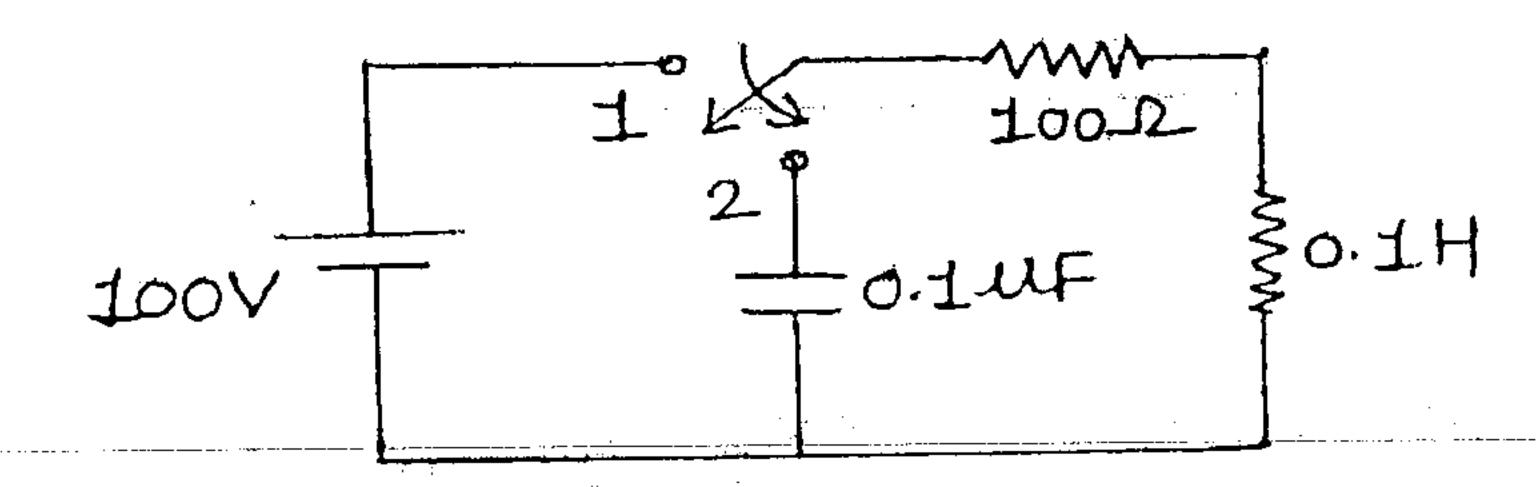
2.(a) For the given tree (shown with firm lines) obtain.

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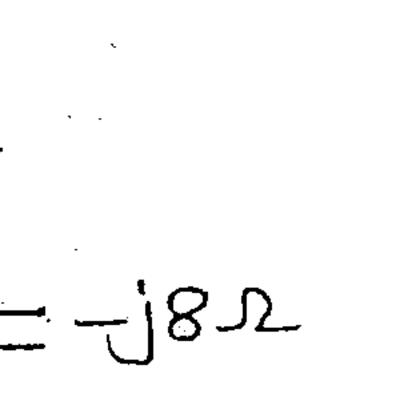
- (i) Incidence matrix
- (ii) Fundamental cutset matrix

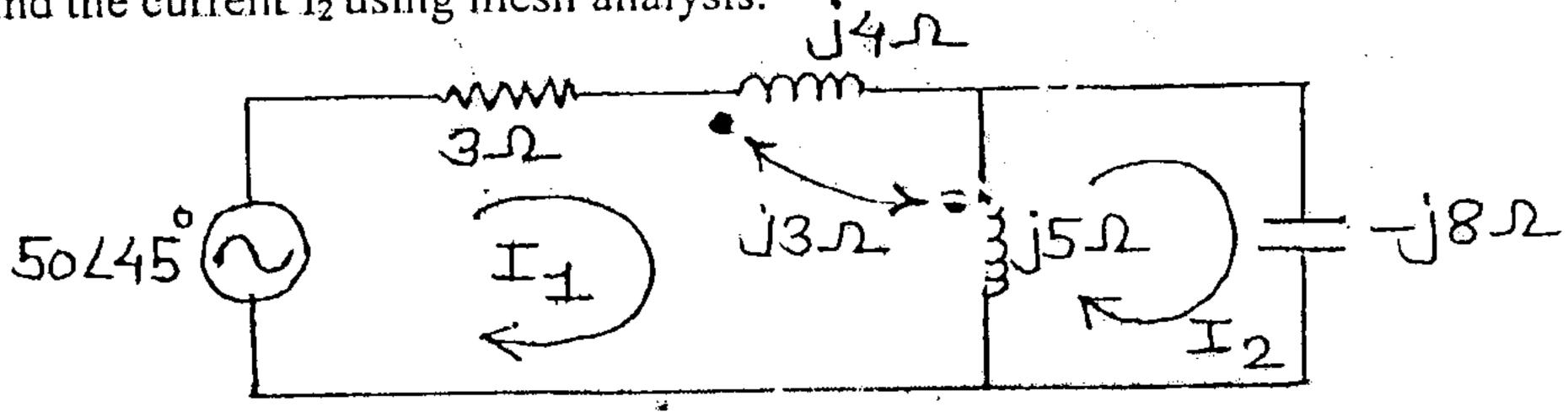


For the given the given network, the switch is changed from position 1 to 2 at time 10 t=0. Find i, $\frac{di}{dt}$ and $\frac{d^2i}{dt^2}$ at t=0⁺. Assume that steady-state is reached at switch position 1.



3.(a) Find the current l₂ using mesh analysis.





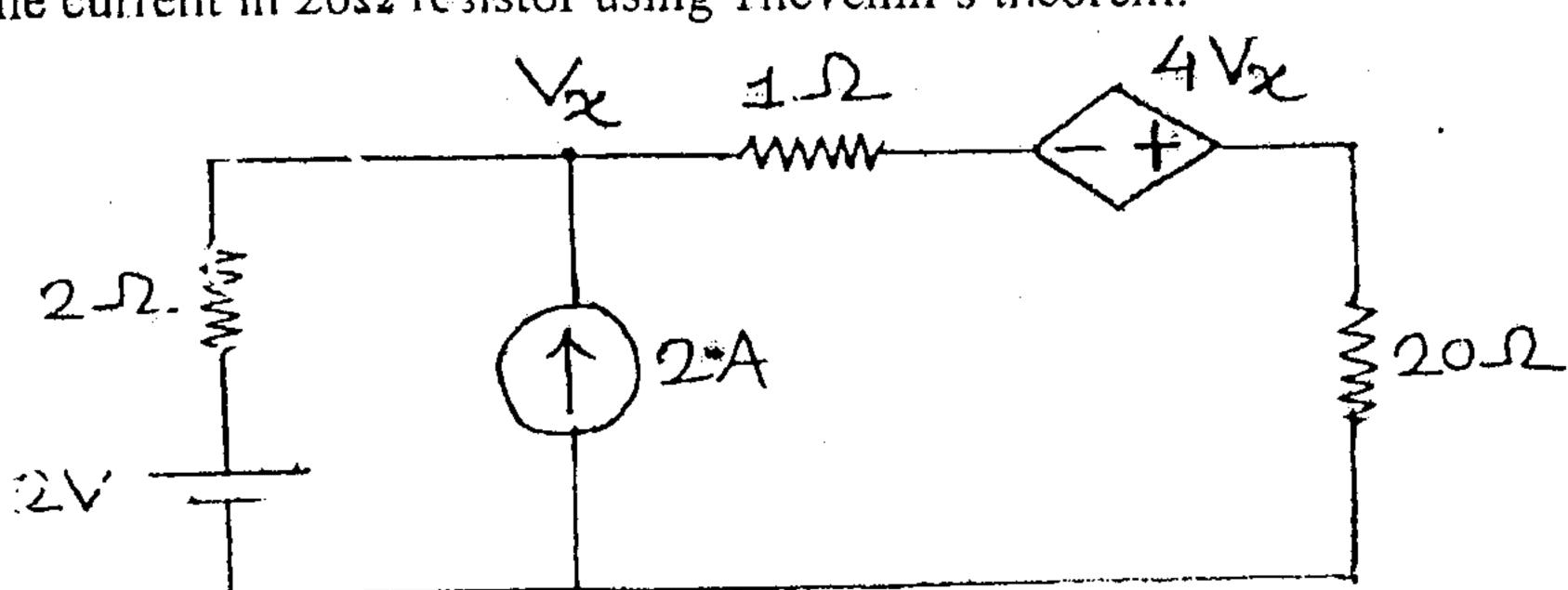
Realize using Foster-l and Foster-II form. (b)

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$$Y(s) = \frac{s(s^2+2)(s^2+4)}{(s^2+1)(s^2+3)}$$

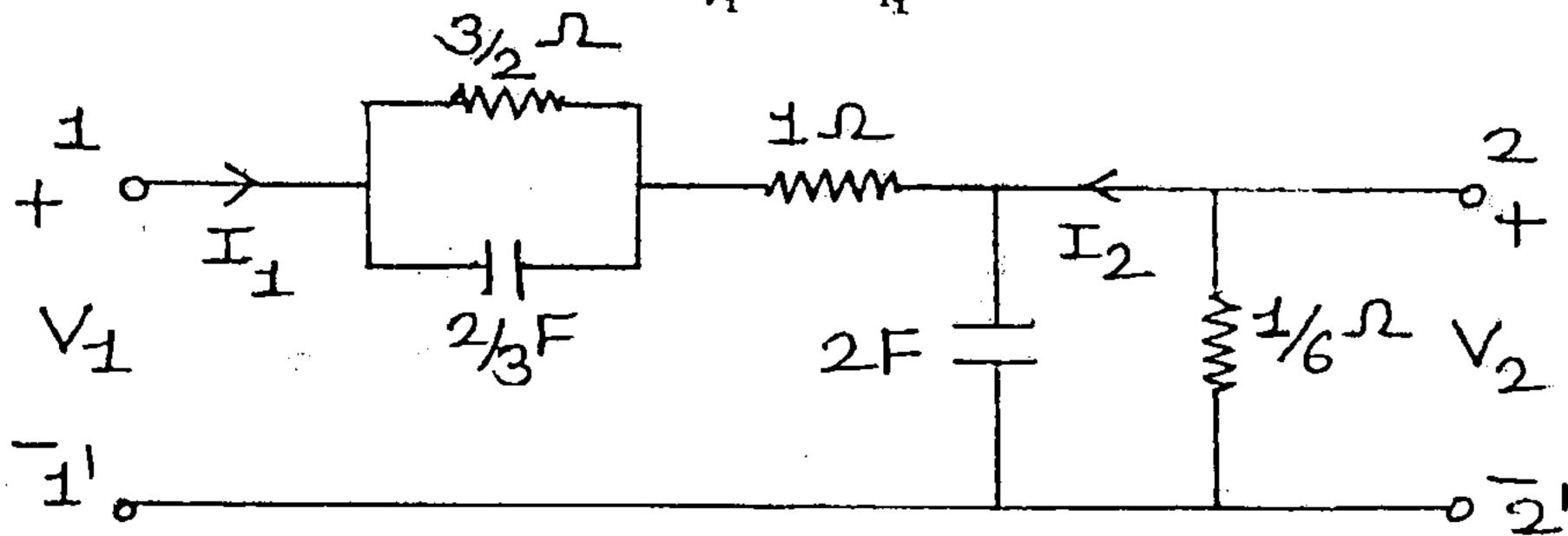
4.(a) Find the current in 20Ω resistor using Thevenin's theorem.

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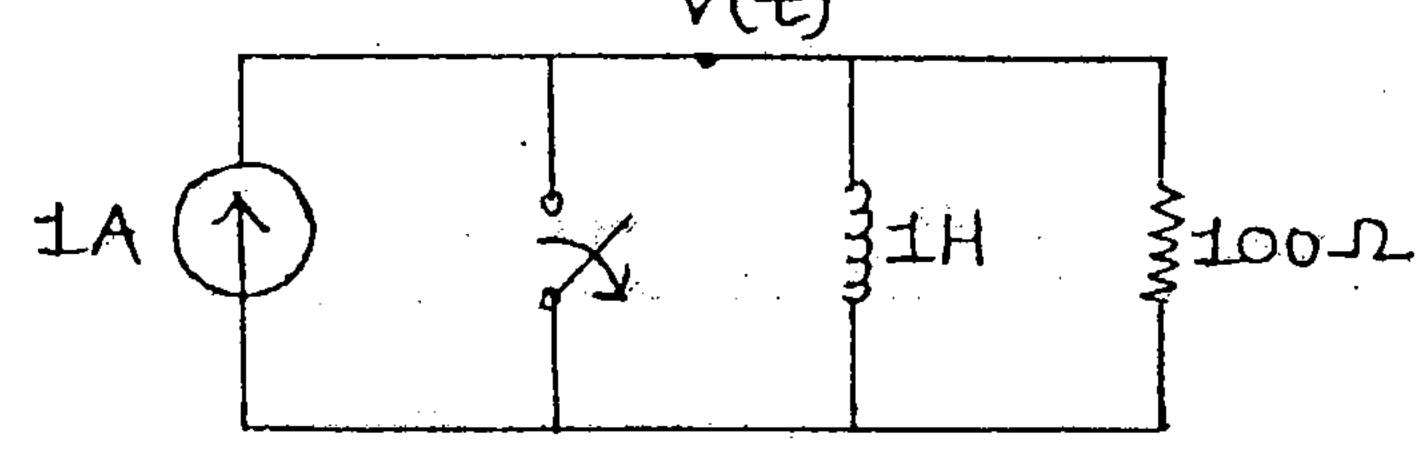
(b) For the given network, find out $\frac{V_2}{V_1}$ and $\frac{I_2}{I_1}$.

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5.(a) For the given network at t = 0, switch is opened. Calculate v, $\frac{dv}{dt}$, $\frac{d^2v}{dt^2}$ at $t=0^+$.

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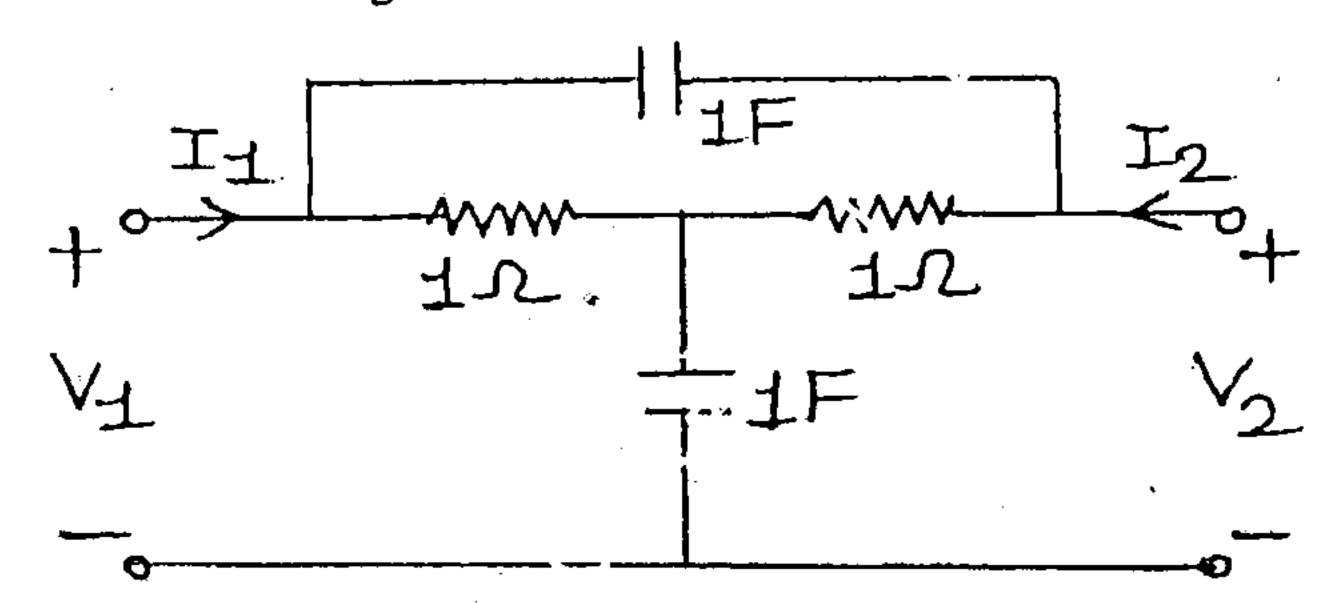
(b) Check the positive realness of the following functions:

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(i)
$$\frac{(s^2+s+6)}{(s^2+s+1)}$$
 (ii) $\frac{(s^2+1)}{(s^3+4s)}$

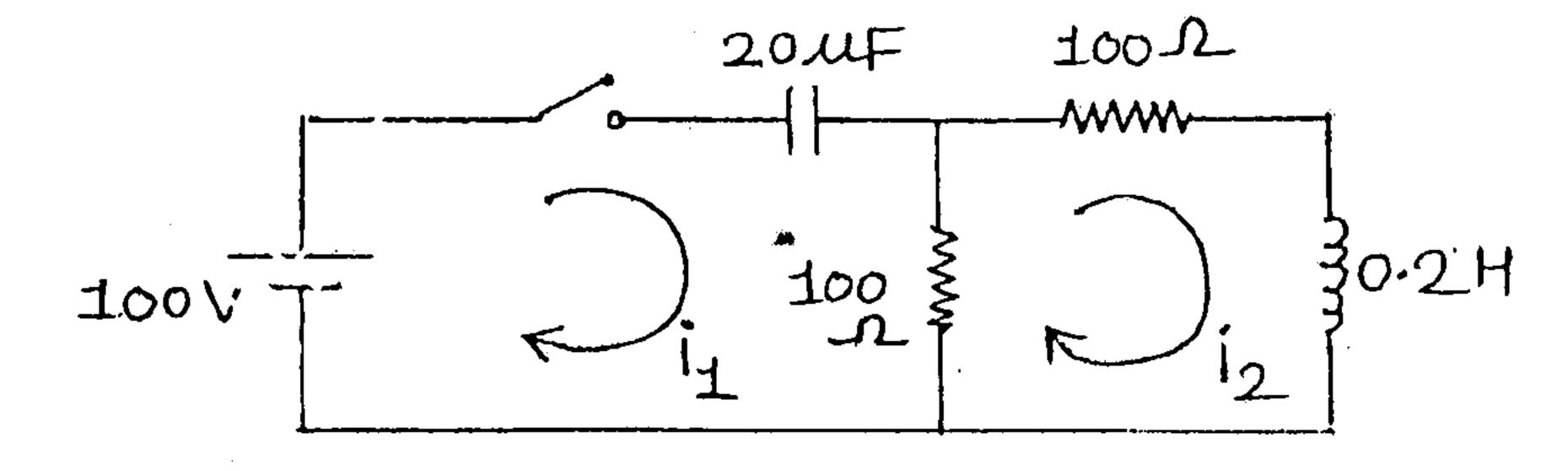
6.(a) Find y parameters for the given network.

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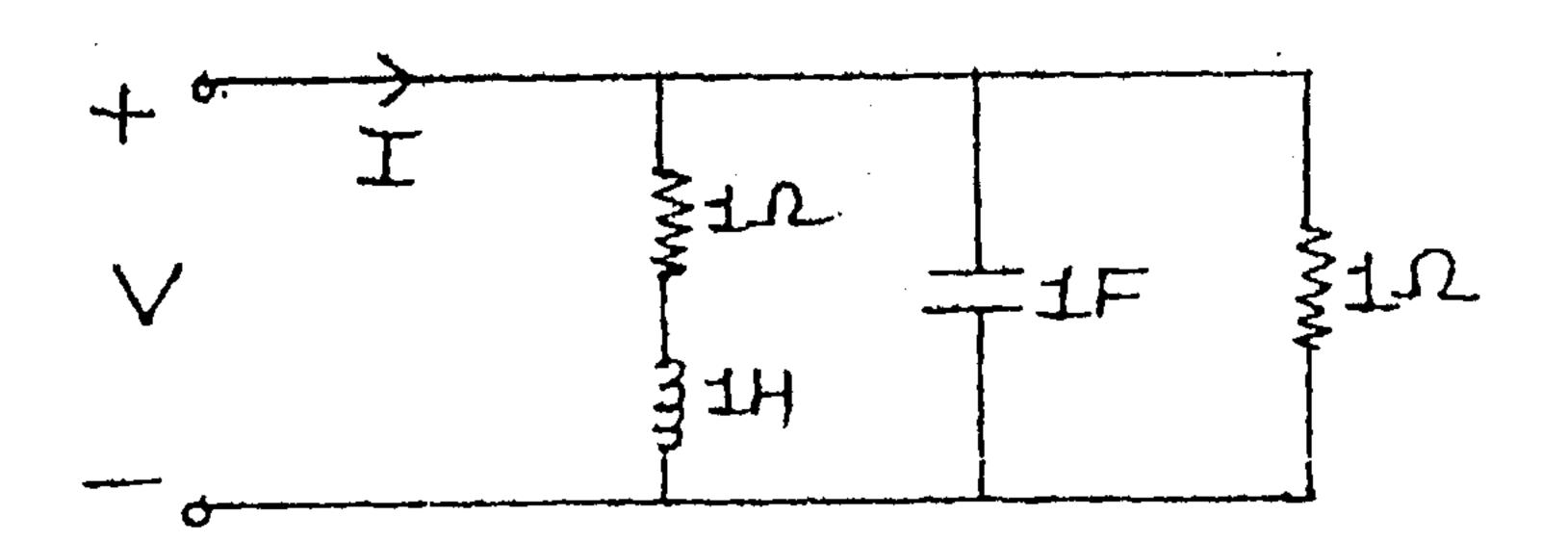


(b) For the network, calculate i₁, i₂, $\frac{di_1}{dt}$, $\frac{di_2}{dt}$, $\frac{d^2i_1}{dt^2}$, $\frac{d^2i_2}{dt^2}$ at t=0+. Switch is closed at t = 0. Initially switch is open.

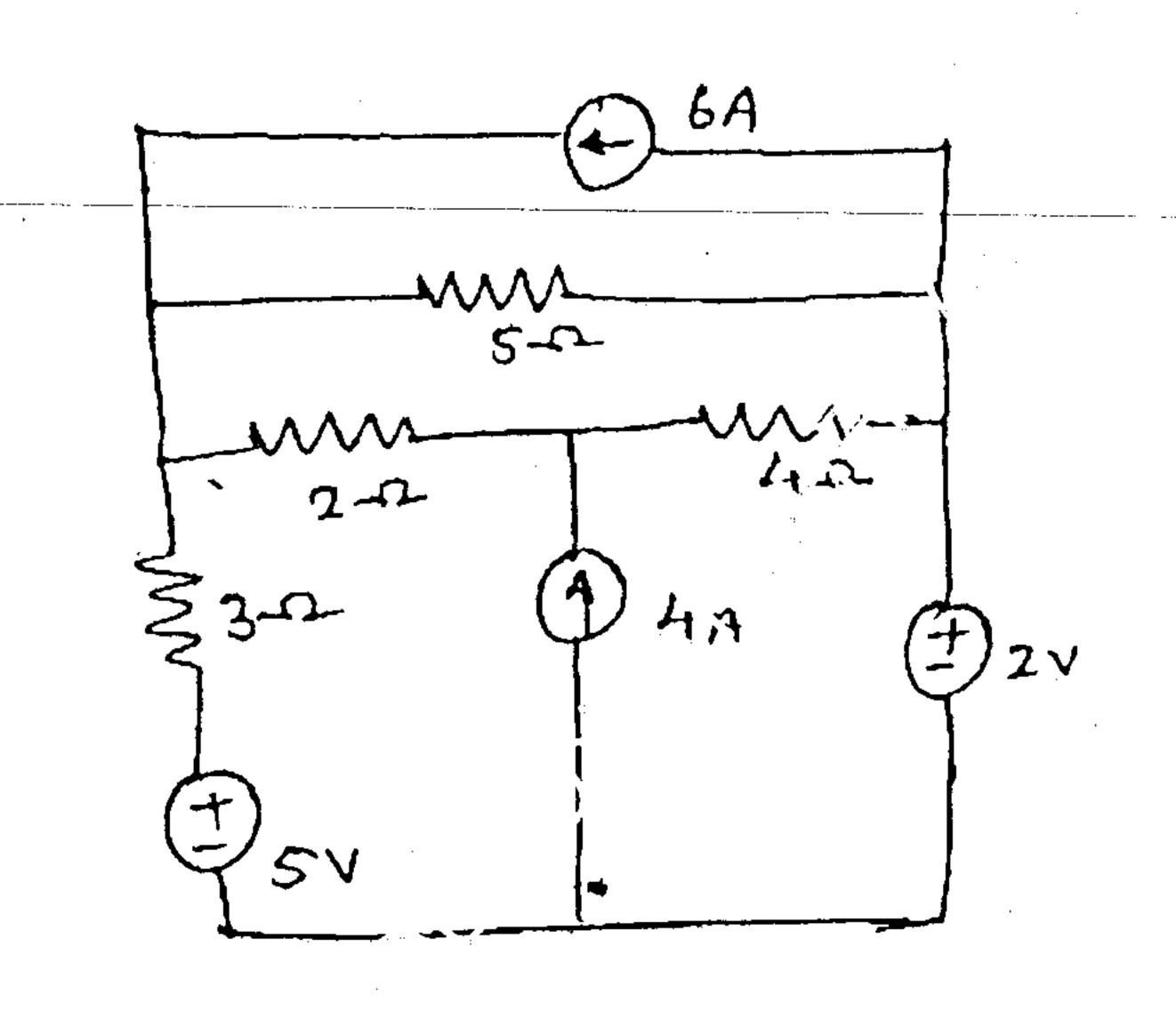
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7. (a) Find the driving admittance Y(s) for the network shown below and plot the pole zero diagram.



(b) Using Nodal method find the current though 4Ω resistor.



(OLD COURSE)

Q.P. Code: 4677

(3 Hours)

Total Marks: 100

N.B.: (1) Question No. 1 is compulsory.

- (2) Attempt any four questions from question no. 2 to 7.
- (3) All sub questions of any question must be answered togethter.
- 1. (a) Find L [Sin t Sin 3t sin 5t]

5

(b) Find z transformation of $\frac{a^k}{k}$, $k \ge 1$

5

- (c) Show that every square matrix A can be uniquely expressed sum of hermitian matrix and skew-hermitian matrix.
- (c) Find the fourier series of $f(x) = \left(\frac{\pi x}{2}\right)^2$ in the interval $0 \le x \le 2\pi$
- 2. (a) Show that $\int_{0}^{\infty} \frac{(\sin 2t + \sin 3t)}{te^{t}} dt = \frac{3\pi}{4}$
 - (b) Show that $A = \frac{1}{2} \begin{bmatrix} \sqrt{2} & -i\sqrt{2} & 0 \\ +i\sqrt{2} & -\sqrt{2} & 0 \\ 0 & 0 & 2 \end{bmatrix}$ is unitary hence find A^{-1} .
 - (c) Find the Fourier Expansion for $f(x) = \sqrt{1 \cos x}$ in (0, 2 π), hence deduce 8
 - $\sum_{1}^{\infty} \frac{1}{4n^2 1} = \frac{1}{2}$
- 3. (a) Solve $(D^2 + 2D + 5)$ $y = e^{-t} \sin t$ given y(0) = 0, y(0)

6

(b) Find the Fourier series of

$$f(x) = \cos x - \pi < x < 0$$

$$\sin x \quad 0 < x < \pi$$

6

(c) Solve the equations by Gauss seidel method

8

$$23x + 4y - z = 32$$

$$2x + 17y + 4z = 35$$

$$x + 3y + 10z = 24$$

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4. (a) P.T.
$$f_1(x) = 1$$
, $f_2(x) = x$, $f_3(x) = \frac{3x^2 - 1}{2}$

(b) Find the non-sigular matrices P and Q such that PAQ is normal. Where A is 6

given by
$$A = \begin{bmatrix} 1 & 2 & 3 \\ 3 & 2 & 1 \\ 1 & 2 & 3 \\ 2 & 1 & 3 \end{bmatrix}$$

(c) Find the inverse Laplace Transformation

(i)
$$L^{-1} \left[log \left(\frac{S^2 + 16}{S^2 + 25} \right) \right]$$
 (ii) $L^{-1} \left[\frac{S + 4}{(S+1)^2 (S-1)} \right]$

5. (a) Find the inverse Z - Transformation
$$f(z) = \frac{1}{(z-3)(z-2)}$$

- (b) Find the fourier series $f(x) = 2x x^2 0 \le x \le 3$
- (c) Investigate for what values of λ , μ the equation x + y + z = 6, x + 2y + 3z = 10 8 $x + 2y + \lambda z = \mu$ have
 - (i) no solution (ii) unique solution (iii) infinite number of solution

6. (a) Find z transformation of Z [a cos
$$k\alpha + b\sin k\alpha$$
] $k \ge 0$

- (b) Find the complex form of Fourier series $f(x) = \cos h a x + \sin h a x in [-\pi, \pi]$ 6
- (c) Find the Laplace Transformation of

(i)
$$L\left[\frac{d}{dt}\left(\frac{1-\cos 2t}{t}\right)\right]$$
 (ii) $L\left[t\sin^3 t\right]$

7. (a) Find the Laplace transformation of
$$f(t) = E \ 0 \le t \le a$$
$$= -E \ 0 \le t \le 2a \ , \ f(t) = f(t + 2a)$$

(b) Obtain half range cos series

$$f(x) = x(\pi - x) \quad 0 \le x \le \pi \text{ and hence deduce } \sum_{1}^{\infty} \frac{1}{n^4} = \frac{\pi^4}{90}$$

RJ-Con. 8877-15.