

Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology
(Autonomous Institute Affiliated to University of Mumbai)

Revision: SPIT-2-17



Bachelor of Engineering/Technology (B.E./B.Tech)
in
Information Technology

Second Year Engineering
(Sem. III and Sem. IV)
Effective from Academic Year 2017 -18

Principal

Principal
Sardar Patel Institute of Technology
Bhavans Andheri Campus
Munshi Nagar, Andheri (West),
Mumbai - 400 058.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Preamble:



Concept of academic autonomy is based on the argument that Institutions can undertake the work expected of them by all stakeholders such as Students , Parents , University , Industry , Society in general, only if they have freedom of choice and action.

We at S.P.I.T. would like to believe that this freedom of choice and action as far as academics is concerned will make us more Proactive in our offerings.

An academic autonomy is as good as its Curricula and execution of it is as well as its faculty. S.P.I.T. is confident of succeeding on both the fronts.

In the first offering we have tried to pro-actively bridge the ever discussed “Industry-academic gap” by way of our SCOPE program. The issue about sensitizing students to social needs is being addressed by special activity based courses. Liberal arts courses have been introduced to enhance functionality of both sides of brain. In all this the professional core has not been overlooked. Thus the curricula are designed to achieve multi dimensional outcomes.

The evaluation mechanism is tuned for assessing the attainment of the designed outcomes and is designed as a fair mechanism.

As our learning cycle begins from July 2017, I wish to place on record that entire S.P.I.T. staff and faculty will work with singular focus and commitment towards the success of this endeavour.

Dr. Prachi Gharpure
Principal, S.P.I.T.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

From the Desk of Dean Academics



Greetings and congratulations to all the students, teaching and supporting staff of Sardar Patel Institute of Technology for getting autonomous status to the institute from the year 2017-18. We look towards autonomy as a great opportunity to design and implement curriculum sensitive to needs of Indian Society and Industries.

In the proposed curriculum we have made an attempt to provide opportunity for students to develop themselves as competent engineering graduates with knowledge, skill and ethical behavior required for global career. Curriculum is designed to provide multiple learning opportunities for students to acquire and demonstrate competencies for rewarding careers. The curriculum model is outcome based that focuses on learning by doing. This is achieved through activity based learning, minor projects, problem solving and innovative styles of pedagogy. Various steps are taken to transform teaching-learning process to make learning a joyful experience for students. Special laboratory based courses are introduced to give more practical exposure to the students.

To create socially responsible citizen curriculum offers courses like Constitution of India, Environmental Studies and Human Health Systems Approach. Also various activity based learning modules like 'Building Automation, Fire Safety and Electronic Security', 'Occupational Safety & Legal Studies for Engineers', 'Technical Presentation Skills', 'Technical Paper and Patent Drafting', 'Engineering Solution for Environmental Problems' and 'Financial Planning, Taxation Policies and Investment' are introduced. For overall development of the learner, various elective courses like Yoga Vidya, Music Appreciation, Dramatics, Industrial and Organizational Psychology, Law for Engineers, French Language, German Language etc. are introduced. To encourage interdisciplinary studies institute level Open Elective courses are offered.

One of the special feature of this curriculum is Skill development programme called SCOPE (Skill Certification for Outcome-Based Professional Education) planned to enhance employability, innovation and research culture in the institute. Every department is offering six domain specific tracks, each track containing six courses. Student will have an opportunity to enroll for more than 140 courses in any of the department of his choice. Some of the courses under SCOPE will be delivered in co-ordination with industries.

We believe that this curriculum will raise the bar of academic standards with the active involvement and cooperation from students, academic and administrative units. Faculty of S.P.I.T. deserves a special appreciation for their relentless efforts in designing curriculum and assessment instruments which will bring transformation in the quality and transparency in assessment of learners.

Looking forward for your active cooperation and constructive feedback to create vibrant and joyful learning environment at Sardar Patel Institute of Technology.

Dr. Surendra Singh Rathod
Professor and Dean Academics



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Message from Head of the Department



Change is the only constant thing in the Information Technology arena. In order to adapt to those changes curriculum design needs to be holistic. Curriculum comprising of Basic Science and Engineering, Professional core and electives are essential. Apart from this, knowledge of other multi-disciplinary domains give a broad outlook. Contemporary issues can have innovative solutions if the problems are seen in different perspectives. Hence open elective courses, liberal arts, legal, ethical practices are included as mandatory courses. To bridge the gap between industry and academia skill development courses are introduced.

Information Technology Curriculum follows the five pillars suggested by ACM as basic streams in our Professional core and electives with strong Information Technology foundation courses. Programming, Networking, Human Computer Interaction, Databases and Web systems are the pillars, all under the umbrella of Information assurance and security.

Dr. Radha Shankarmani

Head of Information Technology Department



Sardar Patel Institute of Technology
 Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
 (Autonomous Institute Affiliated to University of Mumbai)

Scheme for B.E./B.Tech. Information Technology (SEM. III & IV)

SEM III						
Course Code	Course Name	Group	Teaching Scheme (Hrs/week)			Credits Total
			L	T	P	
BS31	Applied Mathematics I*	BS	3	1	--	4
IT31	Advanced Data Structures*	PC	3	--	--	3
IT32	Digital Logic Design and Analysis*	PC	3	--	--	3
IT33	Discrete Structures	PC	3	1	--	4
IT34	Object Oriented Programming*	PC	3	--	--	3
IT35	Information Theory and Coding	PC	3	1	--	4
ITL31	Advanced Data Structures Lab*	PC	--	--	2	1
ITL32	Digital Logic Design and Analysis Lab*	PC	--	--	2	1
ITL34	Object Oriented Programming Lab*	PC	--	--	2	1
ITL36	Open Source Operating System Lab	PC	--	-	2	1
BS32	Human Health Systems Approach (Noncredit)	BS	2	--	--	--
SDX	SCOPE Course (Optional)	SD	--	--	--	--
ABL1	Building Automation, Fire Safety and Electronic Security (Noncredit)	ABL	--	--	--	--
CEP1	Introduction to CEP (Optional)	CEP	--	--	--	--
BC	Fundamentals of Mathematics (Noncredit) (only for direct second year students)	BC	2	--	--	--
Total			20	3	8	25

* Common to Computer and Information Technology Department



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

SEM IV						
Course Code	Course Name	Group	Teaching Scheme (Hrs/week)			Credits Total
			L	T	P	
BS41	Applied Mathematics II*	BS	3	1	--	4
IT41	Design and Analysis of Algorithms*	PC	3	--	--	3
IT42	Computer Organization and Architecture*	PC	3	--	--	3
IT43	Database Management Systems*	PC	3	--	--	3
IT44	Operating Systems*	PC	3	--	--	3
ITL41	Design and Analysis of Algorithms Lab*	PC	--	--	2	1
ITL42	Computer Organization and Architecture Lab*	PC	--	--	2	1
ITL43	Database Management Systems Lab*	PC	--	--	2	1
ITL44	Operating Systems Lab*	PC	--	--	2	1
ITL45	Web Programming Lab	PC	--	1	2	2
LA [^]	Liberal Arts (Non credit) LA1: Yoga Vidya LA2: Music Appreciation LA3: Dramatics	LA	1	--	--	--
SDX	SCOPE Course (Optional)	SD	--	--	--	--
ABL2	Occupational Safety and Legal Studies for Engineers (Noncredit)	ABL	--	--	--	--
CEP2	Problem solving module-I (Optional)	CEP	--	--	--	--
	Total		16	2	10	22

* Common to Computer and Information Technology Department



Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Semester III



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
BS31	Applied Mathematics-I	3	1	--	3	1	--	4
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% Weightage)		

Course Objectives:

- To familiarize learners with mathematical tools and methods to solve engineering problems.

Course Outcomes:

Pre-requisite course codes	BS11 (Engineering Mathematics I) BS21 (Engineering Mathematics II)
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Check analyticity of function of complex variables
	CO2 Find Laplace and Inverse Laplace Transforms
	CO3 Apply Laplace and Laplace Inverse methods to solve differential equations with initial conditions
	CO4 Expand functions in terms of sine and cosine series on the given interval
	CO5 Evaluate Z-transform and Inverse Z-transform
	CO6 Formulate and solve Linear Programming Problem arising in engineering

Module No.	Module name	Unit No.	Topics	Ref	Hrs.
1	Complex Variables	1.1	Analytic functions, Cauchy Riemann equations in Cartesian coordinates and Polar coordinates.	1,2	03
		1.2	Harmonic functions, Analytic method and Milne Thomson methods to find $f(z)$, Orthogonal trajectories.		04
2	Laplace & Inverse Laplace Transform	2.1	Definition of Laplace transform, Laplace transform of constant, trigonometric, exponential functions.	2,4	02
		2.2	Properties of Laplace transform: First shifting theorem, Laplace transform of $L\{t^n f(t)\}, L\{f(t)/t\}, L\{\frac{d^n}{dt^n} f(t)\}, L\{\int_0^t f(u) du\}, L\{f(at)\}$ without proof.		04



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		2.3	Inverse Laplace transform with Partial fraction and Convolution theorem (without proof).		04
		2.4	Application to solving Differential Equations with given initial conditions.		02
3	Fourier series	3.1	Introduction to Fourier Series, Dirichlet's conditions of convergences, Fourier series of periodic functions with period 2π and $2L$.	1,2,3	04
		3.2	Fourier series for even and odd functions		02
		3.3	Half range sine and cosine Fourier series, Parseval's identities (without proof).		02
		3.4	Orthogonal and Orthonormal functions, Complex form of Fourier series.		02
4	Z transform	4.1	Z-transform of standard functions such as $Z(a^n)$, $Z(\cos ak)$, $Z(\sin ak)$, etc.	1,2	01
		4.2	Properties of Z-transform :Linearity, Change of scale, Shifting property, Multiplication of K, Initial and final value, Convolution theorem (all without proof).		02
		4.3	Inverse Z transform: Method of Partial fraction.		02
5	Mathematical Programming	5.1	Introduction to Linear Programming problems and its formulation. Graphical method to solve LPP in two variables, Simplex method to solve LPP	2,3,5	03
		5.2	Artificial variables, Big -M method (method of penalty). Revised and two phase simplex methods.		03
		5.3	Duality, Dual simplex method.		02
Total					42 hrs

References:

1. Kreyszig, "Advanced Engineering Mathematics", 9th edition, John Wiley
2. C. Ray Wylie & Louis Barrett, "Advanced Engg. Mathematics", 6th Edition, New York : McGraw-Hill, c1995.
3. K. B. Datta, "Mathematical Methods of Science and Engineering", 1st edition, Cengage Learning India, 2011
4. M. R. Spiegel, "Laplace Transforms", McGraw-Hill Education (1 January 1965)
5. David G. Luenberger, "Introduction to Linear and Nonlinear Programming", Addison-Wesley Publishing Company.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT31	Advanced Data Structures	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	ES4 (Programming Methodology and Data Structures)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Implement various operations using non linear data structures.
	CO2	Apply concepts of Trees and Graphs to a given problem.
	CO3	Build various Heap Structure
	CO4	Illustrate the hashing and collision resolution techniques.

Module No.	Unit No.	Topics	Ref.	Hrs.
1		Linear and Non-linear Data Structures Introduction to Data Structures (Stack, Queue and Singly Linked List), Circular Linked List, Doubly Linked List, Application of Linked List.	1,2	05
2	2.1	Trees Binary Tree Terminology, Binary Search Tree and its operations, Binary Tree Traversal, Expression Tree	1,2	04
	2.2	AVL Trees- Properties of AVL trees, Rotations, Insertion, and Deletion	1,2	03
	2.3	B-Trees- Definition of B-trees, Basic operation of B-Trees, Deleting a key from B-Trees	1,2	04
	2.4	Introduction to B+ Trees	1,2	03
	2.5	Introduction to Multidimensional Trees, Segment trees, k-d trees, Point Quad trees	3	05
3		Graph Introduction To Graph, Representation of Graph- Adjacency Matrix, Adjacency List, Graph Traversal Technique	1,2	04
4	4.1	Heap Structure Introduction to Heap Structures, Min Heap, Max Heap, Construction of Heap	2	04



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	4.2	Fibonacci heaps- Structure of Fibonacci heaps, Mergeable-heap, operations, Decreasing a key and deleting a node	2	06
5		Hashing Introduction to Hash Table, Hash functions, Collision Resolution Technique	1,2	04
			Total	42

References:

1. Thomas H.Cormen, Charles E. Leiserson, Ronald L Rivest, Clifford Stein, “*Introduction to Algorithms*”, 3rd edition, MIT Press, Massachusetts, 2009.
2. Horowitz E, Sahni S and S.Rajasekaran, “*Fundamentals of Computer Algorithms*”, 2nd edition, Galgotia Publications, New Delhi, 2010.
3. Subrahmanian V S, “*Principles of Multimedia Database Systems*”, 2nd edition, Morgan Kaufman series in Database management systems, USA, 2013.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT32	Digital Logic Design and Analysis	3	--	--	3	--	--	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	ES11 (Basic Electrical and Electronics Engineering)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Design of digital circuits using SOP & POS forms.
	CO2	Construct combinational circuits using given MSI devices.
	CO3	Apply the knowledge of flip-flops and MSI to design counters and Shift registers.
	CO4	Design state machines for given state diagrams after state reduction.
	CO5	Describe different types of programmable logic devices like PAL, PLA, CPLD and FPGA.

Module No.	Unit No.	Topics	Ref.	Hrs.
1	1.1	Introduction to Number System & Digital Logic: Introduction to Number System, Basic gates, Universal gates, Sum of products and products of sum, minimization with Karnaugh Map (up to four variables) and realization. Quine McCluskey method.	1,2,3	16
	1.2	Logic Families: Types of logic families (TTL and CMOS), characteristic parameters (propagation delays, power dissipation, Noise Margin, Fan-out and Fan-in), transfer characteristics of TTL NAND, Interfacing CMOS to TTL and TTL to CMOS.	1,2,3	
	1.3	Combinational Circuits using basic gates as well as MSI devices: Half adder, Full adder, Half Subtractor, Full Subtractor, Multiplexer, De-multiplexer, Decoder, Comparator (Multiplexer and De-multiplexer gate level up to 4:1).	1,2,3	
2	2.1	Sequential Logic: Latches and Flip-Flops. Conversions of Flip-Flops, Timing Considerations and Metastability	1,2,3,4	05
3	3.1	Counters: Asynchronous, Synchronous Counters, Up Down Counters, Mod Counters.	1,2,4,5	11



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	3.2	Mealy and Moore Machines, Clocked synchronous state machine analysis, State reduction techniques and state assignment, Clocked synchronous state machine design.	1,2,4,5	
	3.3	MSI counters and applications.	1,2,4,5	
4	4.1	Shift Registers: Shift Registers, Ring Counters, Universal Shift Register, MSI Shift registers and their applications.	1,2,4,5	05
5	5.1	Programming Logic Devices: Concepts of Programmable Array Logic (PAL) and Programming Logic Array (PLA).	1,2,4,5	05
	5.2	Introduction to Complex Programmable Logic Device (CPLD) and Field Programmable Gate Array (FPGA) architectures.	1,2,4,5	
			Total	42

References:

1. R. P. Jain, “*Modern Digital Electronics*”, 4th Edition, Tata McGraw Hill, 2009.
2. Morris Mano, “*Digital Design*”, 5th edition, Pearson Education, 2013.
3. William I. Fletcher, “*An Engineering Approach to Digital Design*”, 1st Edition, PHI, 2009.
4. John F. Wakerley, “*Digital Design Principles And Practices*”, 3rd Edition Updated, Pearson Education, Singapore, 2002
5. B. Holdsworth and R. C. Woods, “*Digital Logic Design*”, 4th Edition, Newnes, 2002.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT33	Discrete Structures	3	1	-	3	1	-	4
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes		
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Make use of logic and various proof techniques to solve problems.
	CO2	Apply the concepts of set, relations to solve problems
	CO3	Apply the concepts of functions to various technical domains.
	CO4	Solve problems using graphs and trees.
	CO5	Use fundamental concepts of algebraic structures, lattice to solve problems

Module No.	Unit No.	Topics	Ref.	Hrs.
1	1	Logics and Proofs : Predicates, Quantifiers, Propositions, Conditional Propositions, Logical Connectivity, Proposition calculus, Universal and Existential Quantifiers, Equivalence, Normal Forms, Introduction to proofs, Mathematical Induction, Logical inference	1,2	06
2	2.1	Set theory:- Sets, Venn diagram, Operations on set, laws of set theory, partitions of set, types of sets , The principle of Inclusion and Exclusion	1,6	04
	2.2	Relations:- relations, equivalence relation, partial order relation, binary relation, Digraphs, posets and Hasse diagram, recurrence relation, Chains and Anti chains, theorems on chains, transitive closures, Warshall's algorithm	3,6	04



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	2.3	Functions:- Injective, Surjective, Bijective, Inverse, Composition, Identity, Graph of a function. Pigeon-hole principle	1	04
	2.4	Recursive function:- series, sequences, recurrence relation Applications – Divide-and-Conquer algorithm	1	03
3	3.1	Graphs:- Basic terminology, Eulerian graph, Bipartite graph, Hamiltonian graph, planar graph, subgraphs Isomorphism of graph and subgraphs, cliques, connected components, Maximum flow and minimal cut edges, Chromatic number, Graph color problem	4,6	04
	3.2	Applications of Graph theory:- maximum matching using augmenting paths, perfect matching in bipartite graphs, Chinese postman problem,	4,6	04
4	4	Trees:- weighted trees, spanning trees, minimum spanning trees, isomorphism of trees, Kruskal's algorithm for minimal spanning tree. Prim's algorithms for minimal spanning tree.	1,5	03
5	5	Algebraic structures:- semigroup, monoids and groups, Isomorphism, Homomorphism, Automorphism Cyclic groups, Codes and group codes	1,3	05
6	6	Lattice theory: Lattices and algebras systems, principles of duality, basic properties of algebraic systems defined by lattices, distributive and complimented lattices, Boolean lattices and Boolean algebras, uniqueness of finite Boolean expressions, propositional calculus, Coding theory: Coding of binary information and error detection, decoding and error correction	2,3	05
Total				42

References:

1. Kenneth H. Rosen "*Discrete Mathematics and it's applications*", 7th edition, Tata McGraw-Hill
2. Bernad Kolman, Robert Busby, Sharon Cutler Ross, Nadeem-ur-Rehman, "*Discrete Mathematical Structures*", 4th edition, Pearson Education.
3. C. L. Liu, "*Elements of Discrete Mathematics*", 2nd edition, Tata McGraw-Hill, 2002, ISBN: 0-07-043476-X.
4. Douglas B. West, "*Introduction to graph Theory*", 2nd edition, PHI publication.
5. Joe L. Mott, Abraham Kandel, Theodore P. Baker "*Discrete mathematics for computer scientists and mathematicians*", 2nd edition, Reston Publishing Company
6. S.K.Yadav, "*Discrete Mathematics and Graph Theory*" 1st edition, Anne Books Pvt. Ltd



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT34	Object Oriented Programming	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	ES4 (Programming Methodology and Data Structures)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Demonstrate object oriented programming paradigm.
	CO2	Solve problems using inheritance & package.
	CO3	Use file handling concepts in Java for data input and output.
	CO4	Apply concepts of multithreading and exception handling to create efficient program.
	CO5	make use of string and collection classes.

Module No.	Unit No.	Topics	Ref.	Hrs.
1		BASIC OF JAVA	2	
	1.1	History & features, Difference between JDK,JRE,JVM, Unicode system, Advantages of OOP		4
	1.2	Object & Class, Constructor , Command line argument, Static Variable, Method & block		
	1.3	Branching & looping		
2		OOP CONCEPTS	1,2,4	
	2.1	Inheritance (IS – A), Aggregation & Composition (Has – A)		8
	2.2	Method overloading & overriding, Constructor overloading & overriding, this, super, final keyword		
	2.3	Runtime polymorphism, Static and Dynamic Binding		
3		ABSTRACT CLASS, INTERFACE, PACKAGE	1,2	
	3.1	Abstract class & interface, instanceof operator		5
	3.2	Package and access modifier		
	3.3	Object class, Nested class		
4		STRING HANDLING	1,2	
	4.1	Immutable string ,Methods of String class,		4



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	4.2	String comparison, concatenation, substring, toString method		
	4.3	StringBuffer class, StringBuilder class, StringTokenizer class		
5		EXCEPTION HANDLING	1,2	
	5.1	What & why? Try & catch block, Multiple catch block, Nested try, Finally block		5
	5.2	Throw, Throws keywords, Exception propagation		
	5.3	Custom exception		
6		MULTITHREADING	1,2	
	6.1	Life cycle of a – thread, Create thread using, Thread & Runnable class		5
	6.2	Thread methods, schedule, sleep, join, Thread priority, Thread group, Perform multiple task using multiple thread		
	6.3	Thread synchronization		
7		COLLECTION	1,2	
	7.1	Collection framework, ArrayList, LinkedList		5
	7.2	HashMap, Hashtable		
	7.3	Comparable & Comparator		
8		INPUT & OUTPUT	2	
	8.1	FileOutputStream & FileInputStream, BufferedOutputStream & BufferedInputStream, FileWriter & FileReader		6
	8.2	Scanner, PrintStream, PrintWriter, CharArrayWriter		
	8.3	StreamTokenizer class		
			Total	42

References:

1. Ralph Bravaco , Shai Simoson , “Java Programing From the Group Up” , 1st edition, Tata McGraw-Hill.
2. Herbert Schildt, “Java The Complete Reference”, 8th edition, Tata McGraw-Hill.
3. Jaime Nino, Frederick A. Hosch, “An introduction to Programming and Object Oriented design using Java ”,3rd Edition, Wiley.
4. C Xavier, “Java Programming a Practical Approach”, 1st edition, Tata McGraw-Hill.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT35	Information Theory and Coding	3	1	-	3	1	-	4
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes		
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Compute source information based on capacity of channel for transfer of Information.
	CO2	Identify Compression Algorithm to reduce the Size of Information.
	CO3	Find errors in the Information and correct them.
	CO4	Represent Information using number theory systems.
	CO5	Identify cryptographic algorithm to secure information.

Module No.	Unit No.	Topics	Ref.	Hrs.
1	Information Theory and Source Coding		2	06
	1.1	Uncertainty and Information		
	1.2	Entropy		
	1.3	Joint Entropy and Conditional Entropy		
	1.4	Mutual Information		
	1.5	Types of Channel		
	1.6	Prefix Coding		
	1.7	Channel Capacity		
2	Data Compression		4	12
	2.1	Lossy & Lossless Compression Schemes		
	2.2	Run Length encoding		
	2.3	Dictionary Based Compression, LZW		
	2.4	Huffman Coding 2.4.1 Adaptive Huffman Compression		
	2.5	Sliding Window Compression LZ77		
	2.6	Image Compression 2.6.1 GIF 2.6.2 JPEG		
	2.7	Video Compression		



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		2.7.1 Motion Compensation 2.7.2 H.261 2.7.3 MPEG		
3	Error Control Coding Techniques		1	10
	3.1	3.1 Types of Codes Error Checking & Correcting Codes 3.1 Linear Block Codes 3.1.1 Matrix Description of Linear Block Codes 3.1.2 Equivalent Codes 3.1.3 Parity Check matrix 3.1.4 Syndrome Decoding 3.1.5 Error Correction		
	3.2	Cyclic Codes 3.2.1 Polynomials 3.2.2 Matrix Description of Cyclic Codes 3.2.3 Cyclic Redundancy Check (CRC) Codes 3.2.4 Circuit Implementation of Cyclic Codes		
	3.3	Convolutional Codes 3.3.1 Convolutional Encoder 3.3.2 Polynomial Description of Convolutional Codes		
4	Basic Number Theory		3	06
	4.1	Modular Arithmetic		
	4.2	Congruence		
	4.3	Modular Exponentiation		
	4.4	Fermat's Little and Euler Theorem		
	4.5	Prime Number Generation		
	4.6	Random Number Generation		
	4.7	Primitive Roots		
	4.8	Discrete Probability		
	4.9	Discrete Logarithms		
5	Cryptographic Techniques		5	08
	5.1	Security Goals, Threats and Attack on Information		
	5.2	Classic Cryptography		
	5.3	Symmetric Key Cryptography 5.3.1 DES 5.3.2 AES 5.3.3 Triple DES		
	5.4	5.4 Public and Private Key Cryptography 5.4.1 RSA 5.4.2 Diffie-Hellman		
	5.5	Hash Function 5.5.1 MD5 5.5.2 SHA-1 5.5.3 Digital Signature		
Total				42



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

References:

1. Ranjan Bose, "*Information Theory, Coding and Cryptography*", 2nd edition, Tata McGraw Hill, 2008.
2. Thomas Cover, "*Element of information theory*", 2nd edition, Wiley.
3. Ivan nivan , "*An Introduction to Theory of numbers* " , 5th edition, Wiley.
4. Ze-Nian Li& Mark.S.Drew, "*Fundamentals of Multimedia*", PHI publication, 2011.
5. Trappe and Washington, "*Introduction to Cryptogrpahy with Coding theory*", Pearson, 2007.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned				
		L	T	P	L	T	P	Total	
ITL31	Advanced Data Structures Lab	-	-	2	-	-	1	1	
		Examination Scheme							Total
		ISE		ESE		Total			
				Practical	Oral				
		40	10	10	60				

Pre-requisite Course Codes	ES4 (Programming Methodology and Data Structures) IT31 (Advanced Data Structures)
After successful completion of the course, student will be able to:	
Course Outcomes	Student will be able to
	CO1 Implement various Linked List Operations.
	CO2 Implement various Operations of Trees and Graphs.
	CO3 Construct different Heap structures.
	CO4 Analyze different hashing and collision resolution techniques.
	CO5 Choose an appropriate data structure to solve a given problem.

Exp. No.	Experiment Details	Ref.	Marks
1	Implement a given scenario using Linked List.	1,2	5
2	Construct an expression tree using Binary Trees Concept	1,2	5
3	Develop an application to explore the uses of an AVL tree	1,2	5
4	Develop Search application using B-Tree.	1,2	5
5	Demonstrate an application using B+ Tree	1,2	5
6	Implement Operations of Heap Structures	2	5
7	Implement hash functions with different collision resolution techniques	1,2	5
8	Traverse a Graph using Graph Traversal Technique	1,2	5
Total Marks			40

References:

1. Thomas H.Cormen, Charles E. Leiserson, Ronald L Rivest, Clifford Stein, "Introduction to Algorithms", 3rd edition, MIT Press, Massachusetts, 2009.
2. Horowitz E, Sahni S and S.Rajasekaran, "Fundamentals of Computer Algorithms", 2nd edition, Galgotia Publications, New Delhi, 2010.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
ITL32	Digital Logic Design & Analysis Lab	-	-	2	-	-	1	1
		Examination Scheme						
		ISE		ESE		Total		
				Practical	Oral			
40		10		10	60			

Pre-requisite Course Codes	ES11 (Basic Electrical and Electronics Engineering) IT32 (Digital Logic Design and Analysis)
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Construct and test logic circuits using logic gates to realize given function.
	CO2 Construct and Test logic circuits using MSI ICs to realize given function.
	CO3 Validate and test the design of combinational and sequential logic circuits by hardware implementation.
	CO4 Develop an application using concepts of digital circuits.

Exp. No.	Experiment Details	Ref.	Marks
1	To implement the combinational logic for given function using basic gates/MSI ICs.	1,2	5
2	To interface TTL and CMOS logic families with each other.	1,2	5
3	To implement 4-bit, 5-bit and 8 bit comparator using MSI ICs	1,2	5
4	To implement gate level multiplexers and MSI multiplexers	1,2	5
5	To design and implement MSI circuits of flip-flops	1,2	5
6	To design and implement mod 4 synchronous up/down counter using JK flip-flop	1,2	5
7	To configure MSI devices as asynchronous counter, synchronous counter as synchronous counters and universal shift register.	1,2	5
8	Mini-Project: Design and implement an application using digital circuit concepts.	1,2	5
Total Marks			40

References:

1. For datasheet refer: <http://www.datasheetcatalog.com>.
2. R. P. Jain and M. M. S. Anand, "Digital Electronics Practice Using Integrated Circuits," Tata McGraw Hill Education.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned				
		L	T	P	L	T	P	Total	
ITL34	Object Oriented Programming Lab	-	-	2	-	-	1	1	
		Examination Scheme							Total
		ISE		ESE		Total			
				Practical	Oral				
		40		10	10	60			

Pre-requisite Course Codes	ES4 (Programming Methodology and Data Structure) IT34 (Object Oriented Programming)
After successful completion of the course, student will be able to:	
Course outcomes	CO1 Demonstrate object oriented programming concepts for a scenario.
	CO2 Apply static and dynamic binding.
	CO3 Apply concept of input, output and JDBC.
	CO4 Apply multi threading and exception handling for a scenario.
	CO5 Design a java application using J2EE, Swing etc

Exp. No.	Experiment Details	Ref.	Marks
1	Program using conditional and control statements.	1,2,3,4	5
2	Program on Polymorphism.	1,2,3,4	5
3	Program on Inheritance, Aggregation, Composition	1,2,3,4	5
4	Program on Package, Nested Class, String, Collection	1,2,3,4	5
5	Program on Exception Handling	1,2,3,4	5
6	Program on Multithreading, I/O, JDBC	1,2,3,4	5
7	Mini project using Struts, J2EE, Swing etc	2,5,6	5
Total Marks			40

References:

- Ralph Bravaco, Shai Simoson, "Java Programming From the Group Up", 1st edition, Tata McGraw-Hill.
- Herbert Schildt, "Java - The Complete Reference", 8th edition, Tata McGraw-Hill.
- Jaime Nino, Frederick A. Hosch, "An introduction to Programming and Object Oriented Design using Java", 3rd edition, Wiley Student Edition.
- C Xavier, "Java Programming A Practical Approach", 1st edition, Tata McGraw-Hill.
- James Holmes, "Struts: The Complete Reference", 2nd edition, Tata McGraw-Hill.
- Jim Keogh, "J2EE: The Complete Reference", 1st edition, Tata McGraw-Hill.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
ITL36	Open Source Operating System Lab	-	-	2	-	-	1	1
		Examination Scheme						
		ISE		ESE		Total		
				Practical	Oral			
		40		-	20	60		

Pre-requisite Course Codes		
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Demonstrate the installation process of open source operating systems.
	CO2	Use different Linux commands to manage files and directories
	CO3	Demonstrate responsibilities performed by the system administrator.
	CO4	Identify and use UNIX/Linux utilities to create and manage Linux Processes
	CO5	Use common decision constructs in shell scripts effectively
	CO6	Develop a Customized Linux Kernel

Exp. No.	Experiment Details	Ref.	Marks
1	Installation of open source operating systems or automating the installation process.	3,4	5
2	Exploring Linux file system commands-1: <ul style="list-style-type: none"> Viewing Files and Directories Displaying the Contents of Text Files Searching for Text Within Files Finding Files Linking Files. 	1,3,4	5
3	Exploring Linux file system commands-2: <ul style="list-style-type: none"> I/O redirection Filters Awk. 	2,3,4	5
4	Exploring Linux System Administration task and managing Linux processes	2,3,4	5
5	Techniques for <ul style="list-style-type: none"> Compression System Backup Package management 	1,2,3,4	5



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

6	Managing Environment Variables and User-Defined Variables	1,2,3,4	5
7	Program based on shell scripting	2,3,4	5
8	Compile Linux kernel from source to build custom kernel	2,3,4	5
Total Marks			40

References:

1. William E. Shotts Jr, "*The Linux Command Line: A Complete Introduction*", 1st Edition
2. Peter Seebach, "Beginning Portable Shell Scripting: From Novice to Professional (Expert's Voice in Open Source)" Paperback – November 20, 2008 ,
3. Jason W. Eckert, "Linux+ Guide to Linux Certification",
4. Michael Turner , "*Red Hat Linux Administration: A Beginner's Guide*", 1st Edition



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
BS32	Human Health Systems Approach	2	-	-	Non-Credits			
		Examination Scheme						
		ISE1		ISE2		Attendance		Total
		20		20		10		50

Student will be evaluated after completion of 50% syllabus for 20 Marks (ISE1) and at the end of course for 20 Marks (ISE2). Grade equivalent to 'D' (50%-59.99% Marks) or above is considered as 'Satisfactory'. If any of the tasks given is not completed/submitted/shown/evaluated then the corresponding lower grade will be given. Although the grades are given they will not mentioned in final grade card but they are necessary to declare the successful completion of the Non-Credit course.

Pre-requisite Course Codes		
After successful completion of the course, student will be able to understand :		
Course Outcomes	CO1	Physiology as integrated interdisciplinary Science
	CO2	Physiological significance of balanced diet and exercise in health
	CO3	Significance of cleanliness and hygiene in daily routine
	CO4	Dynamics and homeostasis of human health

Module No.	Unit No.	Topics	Ref.	Hrs.
1	Levels of Organizational Systems		1	2
	1.1	Molecular, Cellular and Organ Systems		
	1.2	Biological Molecules		
	1.3	Biochemistry, Biophysics, Molecular Biology and Bioengineering		
2	Energy and Molecular Supply Chain Management		1	7
	2.1	Digestive System: Nutrient supply and Balanced Diet		
	2.2	Respiratory System and effects of Pollution		
	2.3	Cardiovascular System, Blood Pressure, ECG and Blood Report		
	2.4	Musculo-skeletal System and exercise Physiology		
3	Body Fluid Dynamics		1	4
	3.1	Body fluids		
	3.2	Kidneys as Filtration Units and their Physiological Functions		
	3.3	Urinary System		
	3.4	Kidney and Urinary Stones, and Dialysis		
4	Control, Coordination and Regulatory Systems		1	4
	4.1	Sense Organs		
	4.2	Nervous systems		
	4.3	Endocrine Systems (Pancreas and Diabetes, Thyroid and its functions)		



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

5	Defence Systems		1	3
	5.1	Integumentary System		1
	5.2	Immune System		2
6	Molecular Biology and Genetical Information		1	6
	6.1	Hereditary Molecules: DNA RNA		2
	6.2	Horizontal flow of Genetic Information		2
	6.3	Vertical flow of Genetic Information		2
ISE2				1
			Total	26

References:

1. Text book of Anatomy and Physiology for Nurses and allied a health Sciences by Indu Khurana & Arushi.
2. Simplified Courses in Molecular Biology by V.K Agarwal- S.Chand Publications.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		L	T	P	L	T	P	Total
BC	Fundamental of Mathematics	2	-	-	Non-Credits			
		Examination Scheme						
		ISE1		ISE2		Total		
		20		20		100 (60% Weightage)		

Student will be evaluated after completion of 50% syllabus for 20 Marks (ISE1) and at the end of course for 20 Marks (ISE2). Grade equivalent to 'D' (50%-59.99% Marks) or above is considered as 'Satisfactory'. If any of the tasks given is not completed/submitted/shown/evaluated then the corresponding lower grade will be given. Although the grades are given they will not mentioned in final grade card but they are necessary to declare the successful completion of the Non-Credit course.

Course Objectives: To improve the basic mathematical skills for solving engineering problems.

Course Outcomes:

After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Find basic derivatives, Integration and limits.
	CO2	Find rank of a matrix and solve system of linear equations using rank.
	CO3	Find partial derivative of a function and apply it to extremise functions.
	CO4	Solve differential equations of first and higher order.
	CO5	Find roots & logarithm of a complex number.

Module No	Module name	Unit No.	Topics	Ref.	Hrs.
1.	Derivatives	1.1	Derivative of functions which are expressed in one of the following form a) product of functions, b) quotient of functions, c) derivatives of trigonometric function	1,2,5,6,7	1
		1.2	Application of Derivatives: Rolls theorem and Mean value theorem	1,2,5,6,7	1



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

2.	Integration	2.1	Indefinite integrals- methods of integration, substitution method.	1,2,5,6,7	1
		2.2	Evaluation of definite integral 1) by substitution, 2) integration by parts,	1,2,5,6,7	1
3.	Basic of Matrices	3.1	Rank of Matrix, Normal form	1,2,3,4,6	1
		3.2	Consistency and solution of simultaneous linear homogeneous and Non-homogeneous equations. Linear Dependence & independence vectors	1,2,3,4,6	1
4.	Partial Differentiation	4.1	Partial derivatives of first and higher order, Chain Rule & Composite function	1,2,3,4,7	1
		4.2	Euler's theorem on homogeneous functions with two and three independent variables	1,2,3,4,7	1
		4.3	Application of partial derivatives: Maxima and Minima of functions of two variables.	1,2,3,4,7	1
5.	Differential Equations of first & higher order	5.1	Exact Differential Equation,	1,2,3,4,	3
		5.2	Linear Differential Equation with constant coefficient- complementary function, particular integrals of differential equation of the type $f(D)y = X$ where X is		



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

			$e^{ax}, \sin(ax+b),$		
		5.3	$\cos(ax+b), x^m, e^{ax} V,$ $xV.$		
6.	Indeterminate forms	6.1	Indeterminate forms, L- Hospital Rule	7	1
7.	Basics of Complex Numbers	7.1	Roots of complex numbers by De'moivre's Theorem	1,2,3,4	1
		7.2	Relation between circular and hyperbolic function		1
		7.3	Logarithm of complex numbers.		1
				Total	16

References:

1. Dr.B.S.Grewal, "*Higher Engineering Mathematics*" 42nd edition, Khanna Publication, New Delhi.
2. H.K. Das, "*Advanced Engineering Mathematics*" 12th Revised edition, S.Chand Publication, New Delhi, 2004.
3. Erwin Kreyszig, "*Advanced Engineering Mathematics*" 9th edition, John Wiley Eastern Limited, UK.
4. Shanti Narayan, P. K. Mittal, "*A Text book of Matrices*," 11th edition, S. Chand publication, New Delhi.
5. "*Maharashtra state board of secondary and higher secondary education*", edition 2017, Pune.
6. George B. Thomas, Ross L Finney, "*Calculus and Analytical Geometry*" 9th edition, Narosa Publishing House, Mumbai.
7. P.N.Wartikar and J.N.Wartikar, "*A text book of Applied Mathematics*", 9th revised edition, Vol – I and II, VidyarthiGrihaPrakashan, Pune, 2004.



Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Semester IV



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
BS41	Applied Mathematics-II	3	1	--	3	1	--	4
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% Weightage)		

Course Objectives:

- To familiarize learners with mathematical tools and methods to solve engineering problems

Course Outcomes:

Pre-requisite course codes	BS11 (Engineering Mathematics I) BS21 (Engineering Mathematics II)
After successful completion of the course, student will be able to	
Course Outcomes	CO1 Check if matrix is diagonalizable, derogatory & to calculate functions of a square matrix.
	CO2 Find Correlation between two variables.
	CO3 Find the measures of central tendency
	CO4 Solve a problem by identifying the appropriate distribution.
	CO5 Test the hypothesis for means and variances for single and multiple samples using 't' & chi-square distribution tests.

Module No	Module name	Unit No.	Topics	Ref.	Hrs.
1	Matrices	1.1	Eigen values and Eigen vectors and its properties.	1,3,4	04
		1.2	Cayley-Hamilton theorem and its applications.		02
		1.3	Similar matrices, diagonalizable matrices. Singular Value Decomposition.		04



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		1.4	Derogatory and non-derogatory matrices, functions of square matrix.		03
		1.5	Application to finding google page rank		01
2	Probability	3.1	Random Variables: - discrete & continuous random variables, expectation, Variance, Probability Density Function & Cumulative Density Function.	2,3,4,5	04
		3.2	Moments, Moment Generating Function.		02
		3.3	Probability distribution: binomial distribution, Poisson & normal distribution.		07
3	Sampling Theory	4.1	Sampling, point and interval estimations, Test a hypothesis using Large sample test	2,3,4,5	04
		4.2	Testing of hypothesis using 't' and chi-square distribution tests.		04
4	Correlation and Regression	2.1	Karl Pearson's coefficient of correlation, covariance, Spearman's rank correlation		03
		2.2	Regression.	1,3,4	04
Total					42hrs

References:

- [1] Kreyszig, "Advanced Engineering Mathematics", 9th edition, John Wiley
- [2] Kishor S. Trivedi, "Probability & Statistics with reliability", 2nd edition, Wiley India
- [3] C. Ray Wylie & Louis Barrett, "Advanced Engg. Mathematics", 6th Edition, New York : McGraw-Hill, c1995.
- [4] K. B. Datta, "Mathematical Methods of Science and Engineering", First edition, Cengage Learning India, 2011
- [5] Sheldon M. Ross, "Introduction to Probability and Statistics for Engineers and Scientists", 3rd, Elsevier Academic Press, 2004



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT41	Design and Analysis of Algorithms	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	ES4 (Programming Methodology and Data structures)
After successful completion of the course, student will be able to	
Course Outcomes	CO1 Analyze time and space complexity of an algorithm
	CO2 Apply divide and conquer strategy to solve problems
	CO3 Design an algorithm to illustrate the concept of dynamic programming
	CO4 Apply the concept of greedy approach to solve problems
	CO5 Apply backtracking, branch and bound strategy and string matching algorithms to solve some problems.
	CO6 Apply the concept of linear programming to optimize the solution

Module No.	Unit No.	Topics	Ref.	Hrs.
1	1.1	Introduction to analysis of algorithm Performance analysis , space and time complexity Growth of function – Big –Oh ,Omega , Theta notation Mathematical background for algorithm analysis, Analysis of selection sort, insertion sort.	1,2,3	10
	1.2	Recurrences: The substitution method Recursion tree method Master method	1	
	1.3	Divide and Conquer Approach: General method Analysis of Merge sort, Analysis of Quick sort, Analysis of Binary search, Finding minimum and maximum algorithm and analysis, Strassen's matrix multiplication.	1,5	
2	2.1	Dynamic Programming Approach: General Method Assembly-line scheduling 0/1 knapsack Travelling salesman problem Longest common subsequence	1,2,3	12
	2.2	Greedy Method Approach: General Method	1,2,3	



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		Single source shortest path Knapsack problem Minimum cost spanning trees-Kruskal and prim's algorithm Hamming code Algorithm		
3		Backtracking and Branch-and-bound: General Method 8 queen problem(N-queen problem) Sum of subsets Graph coloring 15 puzzle problem, Travelling salesman problem.	1,4	06
4		Linear Programming Standard and slack forms Formulating problems as linear problems The simplex algorithm Duality The initial basic feasible solution	1	08
5		String Matching Algorithms: The naïve string matching Algorithms The Rabin Karp algorithm String matching with finite automata The knuth-Morris-Pratt algorithm	1,5	06
Total			42	

References:

1. T.H .Cormen, C.E. Leiserson, R.L. Rivest, C. Stein, “*Introduction to algorithms*”, 3rd edition, PHI publication 2009.
2. Ellis Horowitz, Sartaj Sahni, S. Rajasekaran. “*computer algorithms*” 2nd edition, Computer Science Press, 1997
3. Sanjoy Dasgupta, Christos H. Papadimitriou, Umesh Vazirani, “*Algorithms*”, 1st edition, Tata McGraw- Hill, 2006.
4. Jon Kleinberg, Eva Tardos, “*Algorithm Design*”, 1st edition, Pearson, 2006.
5. Michael T. Goodrich, Roberto Tamassia, “*Algorithm Design and Application*”, 1st edition ,Wiley Publication, 2015.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT42	Computer Organization and Architecture	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	IT32 (Digital Logic Design and Analysis)
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Describe basic structure of computer.
	CO2 Apply arithmetic algorithm for solving problems.
	CO3 Compare different processor architectures.
	CO4 Describe the memory mapping techniques.
	CO5 Apply I/O concept for simulating I/O device operations.
	CO6 Analyze different parallel processing and pipelining concepts.

Module No.	Unit No.	Topics	Ref.	Hrs.
1		Overview of Computer Architecture & Organization:		
	1.1	Fundamentals of Computer Design : Introduction with Classes of Computers, Defining Computer Architecture, Trends in Technology, Introduction of Computer Organization and Architecture, Basic organization of computer and block level description of the functional units, Evolution of x86 Computers, Von Neumann model, Harvard Model, Embedded system, ARM architecture	1,4	5
	1.2	Performance Issues: Designing for performance, Multicore, GPGPU, Amdahl's Law	1,4	2
2		Data Representation and Arithmetic Algorithms:		
	2.1	Number representation: Floating-point representation, Floating point arithmetic, IEEE 754 floating point number representation	5,6	2
	2.2	Integer Data computation: Addition, Subtraction. Multiplication: Signed multiplication, Booth's algorithm.	5,6	2
	2.3	Division of integers: Restoring and non-restoring division	5,6	2
3		Processor Organization and Control Unit:		
	3.1	CPU Architecture, Register Organization , ISA categories: Complex Instruction Set Computing ISA Features,	2,4	4



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		Reduced Instruction Set Computing ISA Features. Instruction formats, basic instruction cycle. Instruction interpretation and sequencing.		
	3.2	Control Unit: Soft wired (Micro-programmed) and hardwired control unit design methods. Microinstruction sequencing and execution. Micro operations, concepts of nano programming.	2,3	4
	3.3	RISC and CISC: Introduction to RISC and CISC architectures and design issues.	3	1
4		Memory Organization:		
	4.1	Introduction to Memory and Memory parameters. Classifications of primary and secondary memories. Types of RAM and ROM, Allocation policies, Memory hierarchy and characteristics.	3	3
	4.2	Cache memory: Concept, architecture (L1, L2, L3), mapping techniques. Cache Coherency, Interleaved and Associative memory.	2,3	3
	4.3	Virtual Memory: Concept, Segmentation and Paging, Page replacement policies. LRU, FIFO	4,5	4
5		I/O Organization and Introduction to Parallel Processing:		
	5.1	Buses: Types of Buses , Bus Arbitration, BUS standards	5,6	3
	5.2	I/O Interface, I/O channels, I/O modules and IO processor, Types of data transfer techniques: Programmed I/O, Interrupt driven I/O and DMA.	2,5,6	4
	5.3	Introduction to parallel processing concepts, Flynn's classifications, pipeline processing, Pipeline stages, Hazards	5	3
			Total	42

References:

1. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization", 5th edition, Tata McGraw-Hill, 2011.
2. John P. Hayes, "Computer Architecture and Organization", 3rd edition, Tata McGraw-Hill, 2012.
3. William Stallings, "Computer Organization and Architecture: Designing for Performance", 9th edition, Pearson, 2012.
4. B. Govindarajulu, "Computer Architecture and Organization: Design Principles and Applications", 2nd edition, Tata McGraw-Hill, 2010.
5. Dr. M. Usha, T. S. Srikanth, "Computer System Architecture and Organization", 1st edition, Wiley India, 2012.
6. Nicholas P Carter Adapted by Raj Kamal, "Computer Architecture and Organization", 2nd edition, Schaum's Outline, Tata McGraw Hill, 2010.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT43	Database management System	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Design effective database systems, leading to development of elegant Information System.
	CO2 Analyze the real world problem and construct a relational database.
	CO3 Construct a secure database.
	CO4 Design a relation database using concept of functional dependencies.
	CO5 Analyze the effect of concurrency control for transaction processing.

Module No.	Unit No.	Topics	Ref.	Hrs.
1		Introduction Database Concepts and ER Modeling		
1	1.1	Introduction Database Concepts Introduction, Characteristics of databases, File system V/s Database system, Users of Database system, Database Administrator, Concerns when using an enterprise database, Data Independence, codd's Rule, DBMS system architecture,	1,2,3	04
	1.2	ER Modeling		
		Introduction to ER model, Benefits of Data Modeling, Types of data Models, Phases of Database Modeling, The Entity-Relationship (ER) Model, Generalization, Specialization and Aggregation, Extended Entity-Relationship (EER) Model.		04
2		Relational Algebra and SQL	1,2,3	05
	2.1	Relational Algebra Introduction , Mapping the ER and EER Model to the Relational Model , Data Manipulation , Data Integrity , Relational Algebra , Relational Algebra Queries, Relational Calculus.		
	2.2	SQL		



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		Overview of SQL, Data Definition Commands, Set operations, aggregate function, null values, , Data Manipulation commands, Data Control commands , Views in SQL, Nested and complex queries ,PL/SQL		10
3		Relational database design	1,2,3	03
	3.1	Integrity and Security in Database Domain Constraints, Referential integrity, Assertions, Trigger, Security, and authorization in SQL,		
		Normalization	05	
	3.2	Design guidelines for relational schema, Functional dependencies, Normal Forms- 1NF, 2 NF, 3NF, BCNF and 4NF		
4		Transaction Processing	1,2,3	
	4.1	Transactions Management Transaction concept, Transaction states, ACID properties, Implementation of atomicity and durability, Concurrent Executions, Serializability , Recoverability, Implementation of isolation, Concurrency Control: Lock-based ,Timestamp-based , Validation-based protocols, Deadlock handling,		05
	4.2	Recovery System		
		Failure Classification, Storage structure, Recovery and atomicity, Log based recovery, Shadow paging.		03
Total				39

References:

1. Korth, Slberchatz, Sudarshan, “*Database System Concepts*”, 6th edition, McGraw – Hill
2. Elmasri and Navathe, “*Fundamentals of Database Systems*”, 5th edition, PEARSON Education.
3. G. K. Gupta, “*Database Management Systems*”, McGraw – Hill.
4. Peter Rob and Carlos Coronel, “*Database systems Design, Implementation and Management*”, 5th edition, Thomson Learning.
5. Raghu Ramkrishnan and Johannes Gehrke , “*Database Management Systems*”, TMH



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
IT44	Operating Systems	3	-	-	3	-	-	3
		Examination Scheme						
		ISE		MSE		ESE		
		10		30		100 (60% weightage)		

Pre-requisite Course Codes	
	After successful completion of the course, student will be able to:
Course Outcomes	CO1 Describe the basic functions of operating systems.
	CO2 Implement various process scheduling algorithm.
	CO3 Design inter process communication solution
	CO4 Categorize various memory management techniques
	CO5 Apply disk scheduling algorithms.
	CO6 Differentiate among various file systems.
	CO7 Explore functions of operating systems in different environment.

Module No.	Unit No.	Topics	Ref.	Hrs.
1		Introduction to Operating systems		04
	1.1	Introduction to operating system, Functions of Operating systems, Evolution of Operating Systems, RTOS, Mobile OS, Booting.	1	
	1.2	Processes and Interrupts, User mode and kernel mode, Kernel architectures: Monolithic, Micro-kernel.	1,2	
	1.3	Process System Calls.	1,2	
2		Processes and Threads.		09
	2.1	Life cycle of a process, context switching, Process Control Block, Inter-process communication (IPC).	1,2	
	2.2	Process Scheduling: FCFS, RR, SJF , Priority , Comparison of different scheduling policies.	1	
	2.3	Threads and Thread management.	1	
3		Process Synchronization		09
	3.1	Race conditions and Locks, Conditional Variables	1	
	3.2	Mutual Exclusion- Hardware and Software Approaches, Semaphores, Monitors.	1	
	3.3	Examples of synchronization problems: Readers Writers problem, Dining Philosophers problem, Producer Consumer problem.	1	



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

		Deadlocks :principles of deadlock, deadlock detection, Deadlock Avoidance: Bankers algorithm, Deadlock Prevention.		
4		Memory Management		06
	4.1	Basics of Memory Management, Address Space of a Process	1	
	4.2	Paging, TLB cache, Page table design	1	
	4.3	Working set model, Thrashing, Demand Paging, Page Replacement Algorithms: FIFO, LRU, Optimal.	1	
	4.4	Frame Allocation Policies, Kernel Memory Allocation	1	
	4.5	Segmentation	1	
5		File Management		06
	5.1	Files and File Systems, Layers of Abstraction in File Systems	1	
	5.2	Design Choices for File and Directory Metadata	1	
	5.3	File System Consistency	1	
	5.4	Buffer Cache and Memory-mapped Files	1	
	5.5	Unix and Windows file systems	1	
6		I/O Management		05
	6.1	Kernel I/O subsystem	1	
	6.2	Communication and Data Transfer with I/O Devices	1	
	6.3	Secondary Storage Disks, Disk Scheduling and disk scheduling algorithms : FCFS, SSTF, SCAN, CSCAN, LOOK , Disk cache, RAID	1	
7		Explore operating system functions in real time operating system, mobile operating system, distributed operating system and cloud environment.	1	
			Total	42

References:

1. Silberschatz A., Galvin P., Gagne G. “*Operating Systems Principles*”, 9th edition, Willey.
2. Maurice J. Bach, “*The Design of Unix Operating System*”, Prentice Hall.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned				
		L	T	P	L	T	P	Total	
ITL41	Design and Analysis of Algorithms Lab	-	-	2	-	-	1	1	
		Examination Scheme							Total
		ISE		ESE		Total			
		40		Practical	Oral		10	10	60

Pre-requisite Course Codes	ES4 (Programming Methodology and Data structures) IT41 (Analysis of Algorithms)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Compare time and space complexity of different sorting and searching techniques
	CO2	Solve various problems using dynamic programming approach
	CO3	Illustrate the concepts of greedy approach
	CO4	Demonstrate the applicability of backtracking, branch and bound strategies to solve problems in different domains
	CO5	Demonstrate various string matching algorithms

Exp. No.	Experiment Details (Implementation can be in C/C++ Language)	Ref.	Marks
1	Experiment on finding the running time of algorithm Selection sort Insertion sort	1,3	5
2	Experiment based on divide and conquer approach Merge sort Quick sort Binary search	2,3	5
3	Experiment on finding minimum and maximum numbers using divide and conquer approach	1	5
4	Experiment using dynamic programming approach Multistage graphs single source shortest path all pair shortest path 0/1 knapsack Travelling salesman problem Longest common subsequence	1,4	5
5	Experiment based on greedy approach Single source shortest path	1,5	5



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	Knapsack problem Job sequencing with deadlines Optimal storage on tapes		
6	Experiment on minimum spanning tree using Greedy approach	1,2,5	5
7	Experiment using Backtracking strategy 8 queen problem (N-queen problem) Sum of subsets Graph coloring, 15 puzzle problem Travelling salesman problem	2,3	5
8	Implement string matching algorithms The naïve string matching Algorithm The Rabin Karp algorithm The knuth-Morris-Pratt algorithm	1	5
Total Marks			40

References:

1. T.H .Cormen, C.E. Leiserson, R.L. Rivest, C. Stein, “*Introduction to algorithms*”, 3rd edition, PHI publication 2009.
2. Ellis Horowitz, Sartaj Sahni , S. Rajasekaran. “*computer algorithms*” 2nd edition, Computer Science Press, 1997
3. Sanjoy Dasgupta, Christos H. Papadimitriou, Umesh Vazirani, “*Algorithms*”, 1st edition, Tata McGraw- Hill, 2006.
4. Jon Kleinberg, Eva Tardos, “*Algorithm Design*”, 1st edition, Pearson, 2006.
5. Michael T. Goodrich, Roberto Tamassia, “*Algorithm Design and Application*”, 1st edition ,Wiley Publication, 2015.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned				
		L	T	P	L	T	P	Total	
ITL42	Computer Organization and Architecture Lab	-	-	2	-	-	1	1	
		Examination Scheme							Total
		ISE		ESE		Total			
				Practical	Oral				
		40	-	20	60				

Pre-requisite Course Codes	IT32 (Digital Logic Design and Analysis) IT42 (Computer Organization and Architecture)
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Identify the components of Computers and Assemble the computer system.
	CO2 Design ALU operations using Lab View and VHDL tool.
	CO3 Apply data arithmetic algorithms for implementing arithmetic operations
	CO4 Apply various memory management technique for memory allocation and page replacement algorithms
	CO5 Demonstrate I/O operations
	CO6 Analyze the performance of the systems.

Exp. No.	Experiment Details	Ref.	Marks
1	To recognize the components of computer, dismantling and assembling of CPU.	5	5
2	To demonstrate the working of Assembler using NASM.	6	5
3	To simulate the ALU operations using Lab View.	1,7	5
4	To implement various algorithms like Booth's algorithm ,division by restoration and non-restoration for arithmetic operations	2,3	5
5	To implement page replacement and memory allocation algorithms.	2,3	5
6	To implement the mapping techniques of Cache memory.	2,3	5
7	To implement serial communication using RS232.	2,3	5
8	Write a program that simulates the behavior of a pipelined processor using open DLX simulator.	4	5
Total Marks			40

References:

1. Manual to use the simulator for computer organization and architecture. Developed by the Department of CSE, IIT Kharagpur (<http://cse10-iitkgp.virtual-labs.ac.in/>)



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

2. William Stallings, “*Computer Organization and Architecture: Designing for Performance*”, 9th Edition, Pearson, 2012.
3. B. Govindarajulu, “*Computer Architecture and Organization: Design Principles and Applications*”, 2nd edition, McGraw-Hill, 2010.
4. P. López. DLXide web page. <http://www.gap.upv.es/people/plopez/english.html>
5. <https://youtu.be/obSsX7-ZwWc>
6. Steven Armburst & Ted Forgen, “*Programmer’s manual for IBM personal computers*”, Intel handbook, Galgotia Publication Ltd.
7. Rick Bitter, Taqui Mohiuddin, Matt Nawrocki, “*Lab view: Advanced Programming Techniques*”, 2nd edition, CRC Press.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned				
		L	T	P	L	T	P	Total	
ITL43	Database Management System Lab	-	-	2	-	-	1	1	
		Examination Scheme							Total
		ISE		ESE		Total			
				Practical	Oral				
		40	-	20	60				

Pre-requisite Course Codes	IT33 (Discrete Structures) IT43 (Database Management System)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Design a relational database for real world system.
	CO2	Apply SQL commands on database.
	CO3	Execute various transaction and recovery commands over database.
	CO4	Examine effect of concurrency control on database.

Assign a case study for group of 2/3 students and each group to perform on their case study following experiments.

Expt. No.	Experiment Details	Ref.	Marks
1	Create a database for real world system. <ul style="list-style-type: none"> E-R Diagram Mapping of E-R to relational Model Perform database administration related commands DCL 	1,2	5
2	Build a database and populate using SQL. <ul style="list-style-type: none"> DDL DML 	1,2	5
3	Perform various nested queries on database. <ul style="list-style-type: none"> JOIN 	1,2	5
4	Create a reports using view.	1,2	5
5	Perform TCL and Store procedure on database	1,2	5
6	Examine integrity of database using Assertion and Triggers	1,2	5
7	Examine the consistency of database using various concurrency control techniques. <ul style="list-style-type: none"> Creation of serializability schedule. 	1,2	5
8	Check for deadlock condition over database	1,2	5
Total Marks			40

References:

- Sharaman Shah, "Oracle for Professional", 1st edition, SPD, 2013.
- Dr. P.S.Deshpande, "SQL & PLSQL for oracle-10g" Black Book, 2007 edition, Dreamtech publishing.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
ITL44	Operating Systems Lab	-	-	2	-	-	1	1
		Examination Scheme						
		ISE		ESE		Total		
				Practical	Oral			
		40	-	20	60			

Pre-requisite Course Codes	IT44 (Operating Systems)	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Implement process system call in Unix.
	CO2	Apply various process scheduling/disk scheduling algorithm.
	CO3	Implement inter process communication.
	CO4	Implement various file and memory management strategy.
	CO5	Demonstrate BIOS system security using virtual machine.

Exp. No.	Experiment Details	Ref.	Marks
1	Implement process system call in Unix OS.	1,2	5
2	Implement CPU scheduling algorithm.	1	5
2	Implement Producer consumer problem using Semaphore.	1	5
3	Implement Bankers algorithm for deadlock avoidance.	1	5
4	Implement inter process communication	1	5
5	Implement system calls for mv, cp.	1,2	5
6	Implement disk scheduling algorithm	1	5
7	BIOS CMOS and BOOT process : Study BIOS setting security	1	5
8	Implement demand paging.	1	5
Total Marks			40

References:

1. Silberschatz A., Galvin P., Gagne G. "Operating Systems Principles", Willey 9th edition.
2. Maurice J. Bach, "The Design of Unix Operating System", Prentice Hall.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
ITL45	Web Programming Lab	-	1	2	-	-	2	2
		Examination Scheme						
		ISE		ESE		Total		
				Practical	Oral			
40	-	20	60					

Pre-requisite Course Codes	ES4 (Programming Methodology and Data Structures)
After successful completion of the course, student will be able to:	
Course Outcomes	CO1 Design static web pages using HTML5 and CSS3
	CO2 Apply client side scripting to static web pages
	CO3 Build dynamic web pages using server side scripting
	CO4 Develop a web application using web development frameworks

Exp. No.	Experiment Details	Ref.	Marks
1	Static Web Page Design: HTML5 Basic structure of an HTML5 document, Creating an HTML5 document, Mark up Tags, Heading-Paragraphs, line Breaks HTML5 Tags - Introduction to elements of HTML, Working with Text, Lists, Tables and Frames, Hyperlinks, Images and Multimedia, Forms and other HTML5 controls. <i>(Programming assignments based on the above topics)</i>	1,2	5
2	Static Web Page Design: CSS3 Concept of CSS, Creating Style Sheet, CSS Properties, CSS Styling(Background, Text Format, Controlling Fonts), Working with block elements and objects, Lists and Tables, CSS Id and Class, Box Model(Introduction, Border properties, Padding Properties, Margin properties), CSS Advanced(Grouping, Dimension, Display,Positioning, Floating, Align,Pseudo class, Navigation Bar, Image Sprites, Attribute sector), Creating page Layout and Site Designs <i>(Programming assignments based on the above topics)</i>	1,2	5
3	Client side scripting – Javascript Introduction to JavaScript, Lexical Structure, Types, Values, and Variables , Expressions and Operators, Statements, Objects, Arrays, Functions, Pattern matching with regular expressions, Javascript in Web Browsers, The Window object, Scripting Documents, Scripting CSS, Handling Events <i>(Programming assignments based on the above topics)</i>	2	5
4	Client side scripting – JQuery jQuery Basics, jQuery Getters and Setters, Altering Document Structure,	3	5



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

	Handling events with jQuery, Animated Effects, Utility functions, jQuery Selectors and Selection Methods, Extending jQuery with Plugins, The jQuery UI Library <i>(Programming assignments based on the above topics)</i>		
5	Bootstrap Introduction to Bootstrap, downloading and installing Bootstrap, The Grid System: Introducing the Grid, Offsetting and Nesting, Responsive Features, Utility Classes, and Supported Devices, CSS Foundations: Typography in Bootstrap, Styling Tables, Styling Forms, Styling Buttons, Images, icons, and Thumbnails, Navigation Systems: Tabs, Pills, and Lists, Breadcrumbs and Pagination, Navigation Bar, Making the Navigation Bar Responsive, Javascript Effects: Drop-downs, Modal Windows, Tooltips and Popovers, Navigation Aids: Tabs, Collapse, Affix, Carousel, Bootstrap Customization: Combining Elements in Bootstrap, Customizing by Components, Plugins, and Variables <i>(Programming assignments based on the above topics)</i>	4	5
6	Server side-scripting – PHP Introduction to PHP, PHP Tags, Adding Dynamic content, Accessing form variables, Identifiers, user-declared variables, Data types, Constants, Operators, Control structures, Conditionals, Iteration constructs, Using arrays, string manipulation and regular expressions, reusing code and writing functions <i>(Programming assignments based on the above topics)</i>	5	5
7	Server side-scripting – PHP Designing and creating your web database, Accessing MySQL database from the Web with PHP, Session Control in PHP <i>(Programming assignments based on the above topics)</i>	5	5
8	Server side-scripting – Laravel Framework Managing Your Project Controllers, Layout, Views, and Other Assets, Talking to the Database, Model Relations, Scopes, and Other Advanced Features, Integrating Web Forms, Authenticating and Managing Your Users, Deploying, Optimizing and Maintaining Your Application <i>(Programming assignments based on the above topics)</i>	6	5
Total Marks			40

References:

1. J. Millman and A. Grabel, “*Head First HTML and CSS*”, 2nd edition, O’ Reilly, 2012.
2. Ben Frain, “*Responsive Web design with HTML5 and CSS3*”, 2nd edition, PACKT Publishing Ltd.
3. David Flanagan, “*Javascript: The Definitive Guide*”, 6th edition, O’ Reilly, 2011.
4. Jennifer Kyrnin, “*SAMS Teach Yourself Bootstrap in 24 hours*”, 1st edition, Pearson Education, 2016.
5. L. Welling and L. Thomson, “*PHP and MySQL Web Development*”, 5th edition, Addison Wesley, 2016.
6. Martin Bean, “*Laravel 5 Essentials*”, PACKT Publishing Ltd., 2015.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
LA1	Yoga-Vidya	1	-	-	Non-Credits			
		Examination Scheme						
		ISE1		ISE2		Attendance		Total
		20		20		10		50

Student will be evaluated after six sessions for 20 Marks (ISE1) and at the end of last session for 20 Marks (ISE2). Grade equivalent to 'D' (50%-59.99% Marks) or above is considered as 'Satisfactory'. If any of the tasks given is not completed/submitted/shown/evaluated then the corresponding lower grade will be given. Although the grades are given they will not mentioned in final grade card but they are necessary to declare the successful completion of the Non-Credit course.

Pre-requisite Course Codes	---	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Perform various techniques of Yoga
	CO2	Follow healthy habits to improve immune system
	CO3	Describe the importance of Yoga in one's life
	CO4	Make resolution to practice techniques of Yoga

Through this course, students will get an all round experience of how Yoga can benefit their body, breath, emotions along with relaxation techniques to maintain a calm and balanced state of mind.

Day No.	Topics	Hrs.
1	What is Yoga, why Yoga? Techniques: warm up stretches for hands, legs, neck; Sukhasana, Padmasana	1
2	Introduction to Ashtanga Yoga Techniques: Talasana, Utkatasana, Konasana 2	1
3	Asana classification and importance of different types of Asanas Techniques: Sthita-prarathanasana, Ekpadasana, Garudasana	1
4	Yogendra rhythm- breathing pattern Techniques: Parvatasana, Yashtikasana	1
5	Forward bending and abdominal compression Techniques: Konasana 3, Yogamudra	1
6	Shuddhi Kriyas- Prevention of diseases by improving immune system Techniques: Jalaneti, Kapalabhati	1
7	Yogic Aahar Techniques: Vajrasana, Pavanmuktasana	1



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

8	Relaxation and it's importance Techniques: Shavasana	1
9	Spine and it's importance in Yoga Techniques: Bhujangasana, Makarasana, Vakrasana	1
10	Pranayama Techniques: Basic techniques	1
11	Attitude training Techniques: connecting techniques to concepts.	1
12	Pranayama Techniques: Traditional Pranayama	1
13	Yogachara- The Yoga way of living Techniques: Games	1
14	Revision	1

References:

1. Sadashiv Nimbalkar, “*Yoga for Health & Practices*”, Yoga Vidya Niketan, Mumbai.
2. Swami Satyananda Saraswati, “*Asana Pranayama Mudra Bandha*”, Yoga Publications Trust, Munger, Bihar, 2008.
3. Dr.H.R.Nagendra, Dr.R.Nagarathna, “*New Perspectives in Stress Management*”, Vivekananda Yoga Research Foundation, Bangaluru
4. Books from The Yoga Institute, Santacruz:
 - a. Yoga Cyclopedia Vol.11
 - b. Yoga of caring
 - c. Insights through Yoga
 - d. Growing with Yoga



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
LA2	Music Appreciation	1	-	-	Non-Credits			
		Examination Scheme						
		ISE1		ISE2		Attendance		Total
		20		20		10		50

Student will be evaluated after six sessions for 20 Marks (ISE1) and at the end of last session for 20 Marks (ISE2). Grade equivalent to 'D' (50%-59.99% Marks) or above is considered as 'Satisfactory'. If any of the tasks given is not completed/submitted/shown/evaluated then the corresponding lower grade will be given. Although the grades are given they will not mentioned in final grade card but they are necessary to declare the successful completion of the Non-Credit course.

Pre-requisite Course Codes	---	
After successful completion of the course, student will be able to		
Course Outcomes	CO1	Appreciate North Indian Hindustani Music
	CO2	Appreciate the classical music in films

Topics	Hrs.
1. Broad perspective of North Indian of Hindustani Music i.e. a - Classical Vocal b - Semi-Classical Vocal, c - Instrumental - Plucked, Bowed & Wind d - Percussion, e - Families of Musical Instruments. 2. Performance scenario of vocal, instrumental and percussion 3. Film Music where classical music is used	14



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Course Code	Course Name	Teaching Scheme (Hrs/week)			Credits Assigned			
		L	T	P	L	T	P	Total
LA3	Dramatics	1	-	-	Non-Credits			
		Examination Scheme						
		ISE1		ISE2		Attendance		Total
		20		20		10		50

Student will be evaluated after six sessions for 20 Marks (ISE1) and at the end of last session for 20 Marks (ISE2). Grade equivalent to 'D' (50%-59.99% Marks) or above is considered as 'Satisfactory'. If any of the tasks given is not completed/submitted/shown/evaluated then the corresponding lower grade will be given. Although the grades are given they will not mentioned in final grade card but they are necessary to declare the successful completion of the Non-Credit course.

Pre-requisite Course Codes	---	
After successful completion of the course, student will be able to:		
Course Outcomes	CO1	Understand an Art of Theatre.
	CO2	Express their thoughts.
	CO3	Create and visualize new ideas.
	CO4	Perform impressively.

Day No.	Topics	Hrs.
1	Lalitkala (Forms of Art)	1
2	Drama – Show and Text	1
3	Techniques – Abhinay (Acting)	1
4	1. Vachik Abhinay (Reading)	1
5	2. Angik Abhinay (Expressions)	1
6	3. Satvik Abhinay	1
7	Digdarshan (Direction)	1
8	Nepathya (Settings)	1
9	Veshbhusha (Drapery)	1
10	Natyabhasha (Dialogs and Language)	1
11	Kaal and Avakash (Time and Space)	1
12	Natya Rasa (Theory of Rasa)	1
13	Natya Rasa (Theory of Rasa)	1
14	Aswad prakriya	1
Total		14

References:

1. An Actors prepare – Stanislavsky (English)
2. A building a Character – Stanislavsky(English)



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

3. Natyashastra – Bharatmuni (English And Marathi)
4. Abhinaysadhana- K. Narayan Kale (Marathi)
5. Natyavimarsh-K. Narayan Kale (Marathi)
6. Jagatik Rangabhumicha Itihas – Kru. Ra. Sawant (Marathi)
7. Marathi Rangabhumicha Itihas - Shri. Na. Banahatti (Marathi)
8. Lalitkalamimansa- Go. Chi. Bhate(Marathi)
9. Sahitya Adhyapan Ani Prakar- va. la. Kulakarni Gauravgranth(Marathi)
10. Vachik Abhinay- Dr. Shriram Lagoo (Marathi)
11. Rangnayak- Arwind Deshpande



'Activity Based Learning'

ABL1: Building Automation, Fire Safety and Electronic Security

This is non-credit activity conducted in semester III for all the branches of engineering. It is compulsory for all the students to appear for this activity.

This is one day event involving following activities:

1. Invited Talks on the related topics
2. Poster Presentation
3. Fire/Safety Drills
4. Design competition in building automation, fire safety and security.

This event shall be conducted in association with Fire and Security Association of India (FSAI).

ABL2: OCCUPATIONAL SAFETY & LEGAL STUDIES FOR ENGINEERS

Engineering is the branch of science and technology concerned with design, building and the use of engine, machines and structures. In this fast moving world scenario, it is seen that the field of Engineering has travelled a very long distance of time space. In the modern parlance, this field must be properly knitted with the other two important dimensions—SAFETY & LAW. An Engineer must have adequate knowledge of these vital subject if he ever wishes to establish himself in this Industrial world.

The following activities have been designed to meet up with the growing expectations with the concerned topic. Students are requested to actively participate in those activities based learning to catch up with the realities of the industrial world.

Hence, the activities are divided into 5 parts—MOOT COURT, DEBATE, ELOCUTION, PRESENTATION & ROLE PLAY

1) MOOT COURT:

It is a process in which participants take part in simulated court proceedings, usually involving drafting memorials or memoranda and participating in oral argument. It is just a dummy presentation of the actual court proceedings. It will have a pair of counsel from the defendant as



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

well as the prosecution side. The case write up will be given to both the sides well in advance. They need to study the case along the lines of actual law pertaining to the case which will be more or less company law, intellectual property laws, banking laws, insurance laws or negotiable instrument laws. Both sides will have a researcher who will work along with his respective team to unearth any backdated supportive cases. The side has to come in common consensus to work on issues which should not be more than five. Based on those issues both sides will present their case turn by turn with the permission of the judge. Judge can consist of persons ranging from 1 to 3. The issues should be dealt with in the book called memorial which should be prepared by both the sides. The prosecution will start the case followed by the defendants. Nobody can criss-cross each other's time of presentation. The judge/s have to control the whole proceedings properly. The judge has the power to give permission for rebuttal as per his own whims. In the end he will pass the judgment based on law.

2) **DEBATE:**

Debates will have topics based on safety and law based and the pair of participants need to stand for or against the motion.

3) **ELOCUTION:**

Same will be the case with elocution but it will be extempore and the participants will have to present their insights on the topic given on the spot.

4) **PRESENTATION:**

A PPT presentation will have topics based on legal laws and students need to prepare the same.

The list of topics are:

Sale deed, WILL, Gift Deed, Agreement, Power of attorney, MOU(Memorandum of Understanding), Non-Disclosure agreements, Affidavit, Charter, Partnership deed, Copyrights Transfer Agreement, Franchise Termination, Lease purchase contract, Letters Patent, Legal Threat, Promissory Note, Share Certificate, Share transmission.

5) **ROLE PLAY:**

This activity is a group activity whereby they have to work as a team and enact some situation pertaining to law or safety in the Industrial premises. It's a fun activity whereby they camouflage themselves stepping into the shoes of the role that they will be performing to create an awareness amongst the audience of what to do in case they find themselves in same situations in near future.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Competitive Examinations Preparation (CEP): (Extra credits=2)

Salient Features of CEP:

- It is optional module
- Motivation, mentoring and preparation of students to pursue higher education
- Modules as per national level technical competitive examination **GATE**
- Motivation, mentoring and preparation of students to join public sector or government organizations like BARC, DRDO etc.
- Motivation, mentoring and preparation of students to join top ranking technical institutes in country like IISc and IIT.
- Module design as per the courses studies in that semester or prior semester by considering syllabus of GATE examination
- Help to sharpen the problem solving skills of students and concerned teachers
- Course mentors will be allotted at the start of academic year
- Two (2) extra credits will be given if
 1. Student submit **Valid Gate Score card**.
 2. Exam should be taken during the third or fourth year of graduation
 3. Must pass CEP1 to CEP6 in modal question papers given by the faculty
 4. Maintains regular contact with CEP course teachers

CEP Courses:

Semester III:

CEP1: Introduction to CEP

One hour introduction session to entire class about CEP.

Student shall be assigned as 'Teaching Assistant' to Engineering Mathematics Course –I. Student shall maintain regular contact with the semester III course teachers (once in a week per course). Teacher shall maintain the attendance of the student. Teacher shall mentor student and give assignments with GATE level problems to solve. At least TWO assignments per course shall be submitted by the student.

After End Semester Examination student shall appear for the Model Test paper based on the pattern of the actual GATE Examination. This paper shall be based on the contents of Semester-I and Semester-III courses. The negative marking is applicable as per GATE pattern.

The student shall obtain minimum 10 marks to continue registration in CEP module for next semester onwards.

Semester IV:

CEP2: Problem solving module-I

Student shall be assigned as 'Teaching Assistant' to Engineering Mathematics Course –II. Student shall maintain regular contact with the semester IV course teachers (once in a week per course). Teacher shall maintain the attendance of the student. Teacher shall mentor student and give



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

assignments with GATE level problems to solve. At least TWO assignments per course shall be submitted by the student.

After End Semester Examination student shall appear for the Model Test paper based on the pattern of the actual GATE Examination. This paper shall be based on the contents of Semester-II and Semester-IV courses. The negative marking is applicable as per GATE pattern.
The student shall obtain minimum 10 marks to continue registration in CEP module for next semester onwards.

Semester V:

CEP3: Problem solving module-II

Student shall be assigned as 'Teaching Assistant' to Semester III courses.

Student shall maintain regular contact with the semester V course teachers (once in a week per course). Teacher shall maintain the attendance of the student. Teacher shall mentor student and give assignments with GATE level problems to solve. At least TWO assignments per course shall be submitted by the student.

After End Semester Examination student shall appear for the Model Test paper based on the pattern of the actual GATE Examination. This paper shall be based on the contents of Semester-V courses. The negative marking is applicable as per GATE pattern.
The student shall obtain minimum 10 marks to continue registration in CEP module for next semester onwards.

Semester VI:

CEP4: Problem solving module –III

Student shall be assigned as 'Teaching Assistant' to Semester IV courses.

Self Learning: Numerical Ability and Verbal Ability

Student shall maintain regular contact with the semester VI course teachers (once in a week per course). Teacher shall maintain the attendance of the student. Teacher shall mentor student and give assignments with GATE level problems to solve. At least TWO assignments per course shall be submitted by the student.

After End Semester Examination student shall appear for the Model Test paper based on the pattern of the actual GATE Examination. This paper shall be based on the contents of Semester VI courses and self learning module on numerical ability and verbal ability. The negative marking is applicable as per GATE pattern.
The student shall obtain minimum 10 marks to continue registration in CEP module for next semester onwards.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Semester VII:

CEP5: Problem solving module-IV

Student shall be assigned as 'Teaching Assistant' to Semester V courses.

Self Learning: Contents not covered in any semester of study

Student shall maintain regular contact with the semester VII course teachers (once in a week per course). Teacher shall maintain the attendance of the student. Teacher shall mentor student and give assignments with GATE level problems to solve.

At least TWO assignments per course shall be submitted by the student.

After End Semester Examination student shall appear for the Model Test paper based on the pattern of the actual GATE Examination. This paper shall be based on the contents of entire syllabus of GATE Examination. The negative marking is applicable as per GATE pattern.

The student shall obtain minimum 10 marks to continue registration in CEP module for next semester onwards.

Semester VIII:

CEP6: Problem solving module-V

At the start of the semester student shall appear for the TWO Model Test papers based on the pattern of the actual GATE Examination. This paper shall be based on the contents of entire syllabus of GATE Examination. The negative marking is applicable as per GATE pattern.

Student shall submit 'Valid GATE Score Card' after declaration of GATE result.

Mapping with Syllabus for GATE Exam (Semester wise)

Semester - I
Engineering Mathematics-I Matrices, system of linear equations, Maxima and minima.
Programming Methodology & Data Structures: Programming in C. Recursion. Arrays, stacks, queues.
Semester-II
Engineering mathematics –II: Integration,
Semester-III
Discrete Structures: Propositional & first order logic, Sets, Relations, Functions, partial orders & lattices. Groups. Graphs: Connectivity, matching, coloring. Combinatorics: Counting, recurrence relations, generating functions.



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Digital Logic Design & Analysis: Boolean algebra. Combinational and sequential circuits. Minimization. Number representations and computer arithmetic (fixed and floating point).
Advanced data Structure: Linked lists, trees, binary search trees, binary heaps, graphs, hashing.
Semester-IV
Applied Mathematics-II Linear Algebra: Eigen values and eigenvectors, Probability, Random variables. Uniform, normal, exponential, poisson and binomial distributions. Mean, median, mode and standard deviation. Conditional probability and Bayes theorem
Computer Organization and Architecture: Machine instructions and addressing modes. ALU, data-path and control unit. Instruction pipelining. Memory hierarchy: cache, main memory and secondary storage; I/O interface (interrupt and DMA mode).
Analysis Of Algorithms: Searching, sorting, Asymptotic worst case time and space complexity. Algorithm design techniques: greedy, dynamic programming and divide-and-conquer. Graph search, minimum spanning trees, shortest paths.
Operating System: Processes, threads, inter-process communication, concurrency and synchronization. Deadlock. CPU scheduling. Memory management and virtual memory. File systems.
Databases: ER-model. Relational model: relational algebra, tuple calculus, SQL. Integrity constraints, Normal forms. File organization, Transactions and concurrency control.
Semester V:
Automata theory: Regular expressions and finite automata. Context-free grammars and push-down automata. Regular and context-free languages, pumping lemma. Turing machines and undecidability.
Computer Networks: Concept of layering. LAN technologies (Ethernet). Flow and error control techniques, Switching. IPv4/IPv6, routers and routing algorithms (distance vector, link state). TCP/UDP, sockets, congestion control. Application layer protocols (DNS, SMTP, POP, FTP, HTTP). Basics of Wi-Fi.
Semester VI:
Cyber Security: Network security: authentication, basics of public key and private key cryptography, digital signatures and certificates, firewalls.
Not covered in any Semester
Compiler Design: Lexical analysis, parsing, syntax-directed translation. Runtime environments. Intermediate code generation.
Mathematics: LU decomposition. Mean value theorem



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

Self Learning Module: General Aptitude

I. Verbal Ability

1. Grammar
2. Nouns, Pronouns, Articles
3. Verbs, Auxiliaries, Modals
4. Adjectives, Adverbs
5. Prepositions, Conjunctions
6. Active/ Passive Voice, Direct/ Indirect Speech
7. Verbal phrases
8. Sentence Completion
9. Vocabulary
10. Synonyms
11. Antonyms
12. Analogy
13. Reverse Analogy
14. Verbal Reasoning
15. Critical Reasoning
16. Logical Reasoning

II. Numerical Ability

I. Quantitative Aptitude:

1. Simple Equations
2. Ratio-proportion-variation
3. Numbers
4. Percentage, Profit and Loss
5. Simple Interest and Compound Interest
6. Average, mixtures and Alligations
7. Time and Work
8. Time and Distance
9. Indices, Surds, Logarithms
10. Quadratic Equations
11. Inequalities
12. Progressions
13. Permutations and Combinations
14. Data Interpretation

II. Reasoning

1. Number and Letter Series
2. Analogies



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)

3. Odd man out (Classification)
4. Coding and Decoding
5. Blood relations
6. Venn Diagrams
7. Seating Arrangements
8. Puzzles
9. Clocks and Calendars