Con. 2298-08.

(REVISED COURSE)

BB-7081

(3 Hours)

[Total Marks: 100

N.B.: (1) Question 1 is compulsory.

(2) Attempt any Four out of remaining six Questions.

- (3) Assumptions should be made whenever required and should be clearly stated.
- (4) Answers to questions should be grouped and written together.
- (5) Draw the diagrams whenever required.
- 1. (a) For the processes listed below the table, draw Gantt chart and calculate average 12 waiting time and average turn around time using:—
 - (i) FCFS (First come first serve)
 - (ii) SJF(Shortest job First) in both condition -preemptive and non-preemptive
 - (iii) Round-robin (quantum = 2)

| Processes | Arrival Time(ms) | Burst Time(ms) |
|-----------|------------------|----------------|
| PI | 0 | 9 |
| P2 | 1 | 5 |
| P3 | 2 | 7 |
| P4 | - 3 | 3 |

(b) Explain clock hardware and software in I/O System.

8

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- (a) What are external and internal fragmentations? Discuss the techniques to overcome fragmentations.
 - (b) What are the criteria of CPU scheduling? What are the contents of PCB 10 (Process Control Block)?
- 3 (a) Consider following snapshot of a system :-

Available Allocation Max **Processes R1** R2 R3 **R1** R2 R3 R1 R2 R3 7 PO 0 1 0 5 3 3 3 2 P1 2 2 0 3 2 0 P2 3 0 2 9 . 0 2 P3 2 1 2 2 2 1 P4 0 0 2 4 3 3

Us ng banker's aigorithm answers the following:-

- (i) What is the context of matrix need?
- (ii) Is the system in safe state? Give the sequence.
- (iii) If a request from process P1 arrives for (1, 0, 2) can the request be granted immediately?
- (b) What is virtual memory? Explain paging technique in virtual memory. On a simple 10 paging system with 2²⁴ bytes of physical memory, 256 pages of logical address space, and a page size of 2¹⁰ bytes, how many bits are in logical address?

(e)

(f)

Context-switching

Buffering.