S. E. Information technology JEM I

Con. 3009-10.

Deuta Strycture & Algorithms. AN-2569

(3 Hours)

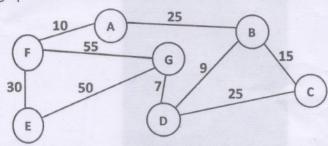
[Total Marks: 100

N.B. 1) Question No. 1 is compulsory	N.B. 1	Question	No. 1 is	compulsory
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- 2) Attempt any FOUR questions from remaining SIX questions.
- 1. a) ·Write a program to implement a STACK ADT using Linked list.
 - b) Explain Huffman Coding and construct Huffman code for the following 10

"JAVA DATA STRUCTURES"

- a) Construct the binary tree for the inorder and post order traversal sequence given below
 In order: "INFORMATION"
 Post order: "INOFMAINOTR"
 - b) Write and explain Radix sort algorithm with suitable example.
- 3. a) Write an algorithm for merge sort and comment on its complexity.
 - b) Calculate and draw the minimum cost spanning tree using Kruskal's algorithm for the 10 following graph.



- 4. a) Explain how Interfaces and Packages are created and accessed with their syntax. 10
 - b) Write any Pattern Matching Algorithm and explain it with suitable example.
- 5. a) Write a program to implement queue using array.
 - b) Write a program to search an element in an array using binary search technique. 10
- 6. a) Write algorithm for heap sort and explain Ascending heap with suitable example. 10
- b) Hash the following in a table of size 11. Use any two collision resolution technique 10
 - 99 67 41 0 17 2 98 20 94 27
- 7. Write short note on any four of the following: 20
 - i) AVL Trees
 - ii) Red and Black Trees
 - iii) Asymptotic Notation
 - iv) Recursion
 - v) Graph traversal technique
 - vi) Abstract data type.

5. E. Informetion technology sem_III

O. Applied Mathematics III AN-2578 35 . 1st half-10-DD (F) Con. 3028-10. 6 and φ, φ and land arks : 100 (3 Hours) φ (2 + (x) φ (2 + (x) (3 Hours)) (3 Hours) (3 Hours) N.B. (1) Question No. 1 is compulsory. (2) Attempt any four out of remaining six questions. (3) Figures to the right indicate full marks. (4) Make suitable assumptions if required and justify the same. (a) Find the orthogonal trajectory of the family of curves $x^3y - xy^3 = c$ (b) Find the Laplace transform of -(i) Expand a function cosz in a Taylor's series \sqrt{t} bout (ii) π \sqrt{t} \sqrt (c) Show that $\left[\operatorname{diag}(\alpha \ \beta \ \gamma)\right]^{-1} = \operatorname{diag}\left(\frac{1}{\alpha}, \frac{1}{\beta}, \frac{1}{\gamma}\right)$ if $\alpha \ \beta \ \gamma \neq 0$. 5 (d) If $f(x) = x - x^2$ for -1 < x < 1 obtain Fourier series of f(x) in (-1, 1). 5 (a) Find the Fourier series for the function f(x) = |x| in $(-\pi, \pi)$ and hence deduce 6 that $\frac{\pi^2}{8} = 1 + \frac{1}{2^2} + \frac{1}{5^2} + \dots$ (b) Show that u = cosx coshy is a harmonic function. Find its harmonic conjugate 7 and corresponding analytic function. of Prove that necessary and ms (0) condition for a square matrix A to poss 7

(c) Determine I, m, n and find A^{-1} if $A = \begin{pmatrix} m & -n \\ -m & n \end{pmatrix}$ is orthogonal.

(a) Show that $\int \log z dz = 2\pi i$ where C is the unit circle in the z plane.

(b) Using Convolution Theorm, find the inverse Laplace transform of $s^2 + 2s + 3$ bortem moternal eagled yd notsupe ent evice (c)

$$(s^2 + 2s + 2)(s^2 + 2s + 5)$$

(c) Show that the set of function cosnx is orthogonal function in $(0, 2\pi)$.

(a) Find the Fourier series for the function f(x) = x in $(-\pi, \pi)$ and hence deduce that $1 - \frac{1}{3} + \frac{1}{5} - \frac{1}{7} + \dots = \frac{\pi}{4}$

(b) Find an analytic function whose real part is $\frac{1}{2}\log(x^2+y^2)$. Also find its imaginary part using Milne Thompson's method.

(c) Using partial fraction method find the inverse Laplace transform of $s^2(s+3)$.

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Con. 3028-AN-2578-10.

- 5. (a) If $f(x) = c_1 \phi_1(x) + c_2 \phi_2(x) + c_3 \phi_3(x)$ where c_1 , c_2 , c_3 are constant and ϕ_1 , ϕ_2 and ϕ_3 are orthonormal sets of function on (a, b). Then show that $\int_{a}^{b} [f(x)]^2 dx = c_1^2 + c_2^2 + c_3^2$ 5) Figures to the right indicate full marks.
 - (b) Find the adjoint of the coefficient matrix of the following system and determine its inverse matrix. Hence solve the system :-

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$$x + y + z = \alpha$$

$$x + 2y + 3z = \beta$$

$$2x + 3y + 8z = \gamma$$

- (c) Expand a function cosz in a Taylor's series about $z = \frac{\pi}{4}$.
- 1 -1 , find two matrices P and Q such that PAQ is in normal form.
 - (b) Evaluate the following using Cauchy Residue Theorem
 - (i) $\int \frac{(z+4)^2}{z^4+5z^3+6z^2} dz$ where c is |z|=1
 - (ii) $\int \csc z dz$ where C is |z| = 1
 - (c) Prove that necessary and sufficient condition for a square matrix A to possess the inverse is that $|A| \neq 0$.
- (a) Solve: 2x y + 3z = 03x + 2y + z = 0x - 4y + 5z = 0
 - (b) Find the Laplace transform of -(i) $e^{4t} \sin^{3t}$ (ii) \sin^{4t} (iii) $(t \sinh_2 t)^2$
 - (c) Solve the equation by Laplace transform method :- + + S + S 6

$$\frac{d^2x}{dt^2} + 4x = 0$$
given x(0) = 1, x¹(0) = -2

Information tech Electronic device & L. circuits. AN-2566 on. 3004-10. (3 Hours) [Total Marks: 100 (1) Question No. 1 is compulsory.(2) Attempt any four questions out of remaining six questions. (3) Assume suitable data if necessary. (4) Figures to right indicate full marks. 1. Answer any Four: 20 (a) State and Explain Barkhausen's criterion for Oscillators. (b) Draw the circuit diagram of Dual input Balanced output Differential amplifier and write formulae for Ad, Ac, CMRR, Rin and Ro. List down the ideal characteristics of OPAMP. The I will de la (c) (d) Draw the Pin Diagram of IC 555, neatly identifying name of each pin and Explain the use of pin no. 5. and find level (e) Draw the neat labeled Diagram of R-2R type DAC. With the help of neat labeled circuit diagram, Explain 2. (a) 10 the working of Practical Integrator . Also Explain its advantages over a simple Integrator. (b) Draw and Explain the working of a Square and Triangular 10 Wave Generator using OPAMP. Design a Wein Bridge Oscillator for a frequency of 1000 Hz. 3. (a) 5 (b) Explain using an OPAMP the operation of a wein Bridge 5 Oscillator. (No Derivation). (c) Explain the working of a Non Inverting Adder using OPAMP. 10 4. (a) 10 Design an Astable multivibrator using IC 555 for fo 5KHz and duty cycle of 75%. Draw and Explain Successive Approximation Register 10 (b) type ADC. 5. (a) Draw and Explain the circuit diagram of First Order 10 Butterworth Low Pass Filter. Design a First Order Low Pass Filter for cut-off frequency of 10 (b)

1KHz and Pass band gain of 10.

Con. 3004-AN-2566-10.

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S.E. Informetion technology sem II

1: 1st hair-Exm. 10-Mina-(e)

Con. 3013-10.

Digital Logic Design & Appliar AN-2572

(3 Hours) [Total Marks: 100

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- N.B. (1) Question No. 1 compulsory.
 - (2) Answer any four questions out of remaining six questions.
 - (3) Assume suitable data if required.
- 1. (a) Convert the following sign-magnitude numbers to a decimal equivalent :-
 - (i) 0011 011 0 (ii) 1010 1110
 - (b) Consider a number FD34H as a input to 16 bit controlled inverter. Give the 5 output of the inverter in hexadecimal notation and binary notation. Design the required circuit. Implement the following Boolian function using 8:1 MUX:-5
 - $F(P, Q, R, S) = \Sigma m(0, 1, 3, 4, 8, 9, 15).$ (d) Show that :-

and minimum number of logic gates.

- (i) $A(\overline{B} + C) = (A + \overline{B} + \overline{C})(\overline{A} + \overline{B} + C)$
 - (ii) $(A + \overline{B} + \overline{C})(\overline{B}\overline{D} + \overline{D}\overline{E}) = (\overline{B} + \overline{E})(\overline{D} + \overline{E}).$
- (a) Design and implement a BCD to excess 3 code converter using suitable decoder 10
 - (b) Convert :-(i) SR F/F to JKF/F
- (ii) JK to D F/F.
- (a) Design and implement 8 x 4 bit ROM using suitable decoder. 10 (b) A and B are the two 2 bit input to the comparator for the following condition: — 10 A = B
 - A < B A > B.

5. (a) Implement the following SOP using PAL and PLA:

$$F_0 = \overline{A} \overline{B} + A \overline{C}$$

$$F_1 = A \overline{C} + B$$

$$F_2 = \overline{A} \overline{B} + \overline{B} \overline{C}$$

$$F_3 = B + AC$$

- (b) Draw the logic diagram for J K master slave configuration. Explain the same 10 using suitable waveform. State its application.
- 6. (a) On certain railway station consisting of seven platforms. For the following situation :— 10
 - (i) train has entered station
 - (ii) train is haulted on platform
 - (iii) train is about to leave the platform

Design a suitable control system for mointoring in the control room.

(b) Explain the essential features of VHDL. Write a VHDL for full adder.

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7. Write short notes on any three of the following :-

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- (a) Carry look-ahead generator
- (c) CAD tools

(b) FGPA architecture

(d) Bidirectional shift registers.

+	m III
Con. 3019-10. Ceci & Data Base Management	AN-2575
(3 Hours) [Total N	larks : 100
N.B.: (1) Question no. 1 is compulsory. (2) Answer any four out of the remaining questions.	,
 (a) What do you mean by E-R diagram ?Draw E-R diagram for university database consisting of four entities: Student, department, Class ,faculty student has a unique id,the student can enroll for multiple classes and has at most one major. 	10
 Faculty must belong to department and faculty can teach multiple classes Each class is taught by only one faculty 	39
 Every student will get grade for the class he/she has enrolled. (b) Describe the overall architecture of a DBMS with a diagram 	10
2. (a) Explain following Relational algebra operators with suitable examples :- (i) Select (ii) Project (iii) Join (iv) Division (v) Cartesion product	10
(b) Give four advantages that a DBMS has over that of a file system.	10
3. (a) Explain the following controls of Visual basic: (i) Listbox (ii) Command button (iii) Combo Box (iv) Message box (v) Checkbox	10
(b) Explain Murphy's law of GUI Design with the help of an example.	10
 (a) Design Active X control for the following GUI screen and explain the procedure for Adding the control to component dialog box 	10
Calendar.OCX Set date	
(b) Discuss ADO and COM Remote Architecture .	10

5.	(a) What do you understand by deadlocks in database system? Explain how it is prevented.	10
	(b) What is transaction? Discuss state transition diagram and properties	
	of transaction.	10
6.	(a) For the given employee database give an expression in SQL for the following : -	
	Employee(empname, street, city)	10
	Works(empname,company-name,salary)	
	Company (company-name, city)	
	Manages (empname, manager-name)	
	(i) Create table ,insert values for all the tables given	
	(ii) Modify the database so that 'jones' now lives in 'Newtown'	
	(iii) Give all employees of First bank corporation a 10 percent raise.	
	(b) Define Serializability ? Explain conflict and view serializability.	10
7.	Write short notes on :-	20
	(i) Validation Based protocol	
	(ii) Total participation, Partial participation, Unique key, primary key and partial k	cey
	(iii) Logbased and checkpoint based recovery mechanisms .	

	/sem II IT/ Rev/ k.	T/ Exam 2010 may
1: 1st hair-Exm. 10-Mina-(e) Con. 3013-10.	Sub: DLDSA.	AN-2572
	(3 Hours)	[Total Marks : 100
	1 compulsory. our questions out of remaining six questible data if required.	stions.

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 (c) Implement the following Boolian function using 8 : 1 MUX :—

5

- F (P, Q, R, S) = Σ m (0, 1,3, 4, 8, 9, 15). (d) Show that :—

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- and minimum number of logic gates.

 (b) Convert :—

 (i) SR F/F to JKF/F

(a) Design and implement a BCD to excess 3 code converter using suitable decoder 10

- (i) SR F/F to JKF/F (ii) JK to D F/F.
- 3. (a) Design and implement 8 x 4 bit ROM using suitable decoder.
 (b) A and B are the two 2 bit input to the comparator for the following condition:— 10

 A = B
 A < B
 A > B.
- 4. (a) For the following sequence :—
 0110 0111010 011
 Design a sequence generator using Mealy method.
 - (b) Design a Mod 5 asynchronous counter using T flip-flop. What is a gliten problem? 10 How is it over comed?

10 5. (a) Implement the following SOP using PAL and PLA:— $F_0 = \overline{A} \overline{B} + A \overline{C}$ $F_1 = A \overline{C} + B$ $F_2 = \overline{A} \overline{B} + B \overline{C}$ $F_3 = B + AC$ (b) Draw the logic diagram for J K master slave configuration. Explain the same 10 using suitable waveform. State its application. 6. (a) On certain railway station consisting of seven platforms. For the following situation: — 10 train has entered station (ii) train is haulted on platform (iii) train is about to leave the platform Design a suitable control system for mointoring in the control room. 10 (b) Explain the essential features of VHDL. Write a VHDL for full adder. 20 7. Write short notes on any three of the following :-(c) CAD tools (a) Carry look-ahead generator (d) Bidirectional shift registers. (b) FGPA architecture