B.E C.ETRX), SEIXI: VIII REVISED D+: 30/05/K/
Embedded Systems and Real Time Programing

QP Code:631300

(3 Hours)

[Total Marks:100]

	N.B.: 1.Question no.1 is compulsory and attempt any 4 from remaining 6 questions 2.Assume suitable data whereever necessary	
Q1.	 (a) Compare RISC and CISC architectures (b) Compare C-programing and assembly programming (c) What are the requirements, challenges, constraints of real time systems (d) Describe any two serial communication methods 	5 5 5
Q2.	(a) Explain MSP 430 features which supports lowpower applications 💨	10
•	(b) Explaing High performance features of ARM 7TDMI based Processors	10
Q3 .	(a) Compare interrupt structure of ARM and MSP430	10
	(b) With the help of suitable examples explain modifiers, macros, List &ordered list	10
Q4.	(a)What is multitasking? What is need of IPC and synchronization	10
	(b) What is priority inversion problem? Suggest solutions for the same	10
Q 5.	(a) Explain addressing modes of ARM and MSP430	10
	(b) Explain any three types of IPC	10
Q6 .	Design a tea vending machines for dispensing tea against Rs.5 coin.	20
	The system should have necessary, minimal, lowcost features.	
	Draw block diagram, System model(FSM/Petrinet), Software architecture, list of	
	components	
~ ~		
Q7.	Write short notes on	E
	(a) Controller Area Network (CAN)	D m
	(b) Interrupt Vectors, Priorities and Nesting	
	(c) List and ordered list in c-programing	5
	(d)System on chip (SOC)	5

Q.P. Code: 630702

Total Marks:100 (3 Hours) N.B.: (1) Question No. 1 is compulsory, (2) Attempt any four out of remaining six questions, (3) Assume any suitable data whenever required and justify the same. Draw and explain Carry save adder Design SR flipflop using AOI, write Verilog HDL (b) Explain electromigration effect in an interconnect. (c) Write Verilog code for 8 bit counter. Write verilog code for 8 bit counter.

What would be the conductor width of power and ground wires to a (d) 50 MHz clock buffer that drives 100 pF of on-chip bad to satisfy the metal-migration consideration ($J_{AL} = 0.5 \text{mA/}\mu\text{m}$)? What is the ground bounce with chosen conductor size? The module is 500 µm from both the power and ground pads and the supply voltage is 5 volts. The rise/fall time of clock is 1 ns. (Assume sheet resistance of wire = $0.05\Omega/\text{sq}$). Draw 1T DRAM cell and explain his write, read, hold and refresh 10 (b) operation. Explain 4-bit CLA adder with its carry equations, logical network and writs its Veri log description. Explain in detail the input protection circuit for CMOS, also explain (b) output circuit with LO circuit Give and explain the maximum and minimum frequency calculation of 10 clock signal which determine the data transfer rate through cascade system. Explain EEPROM using floating gate NMOSFET. Give various important parameters affecting switching performance of 10 CMOS circuit. Suggest method to improve it.

Give and explain single phase clock system and explain its drawback. 10

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- 6. (a) Explain various techniques of clock generation and clock stabilization.
 (b) What is cross talk in IC's? Explain various methods to reduce it.
- 7. Write short notes on (any three)
 - (a) Frequency compensation in CMOS operational amplifier.
 - (b) MODL.
 - (c) H tree clock distribution.
 - (d) Reliability issues in CMOS circuits

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6.5.5b

Robotics and Automation

Q.P. Code: 631202

		(3 Hours) [Total Marks:	100
N.B	•	 Question No.1 is compulsory. Solve any Four from remaining Six questions. Assume suitable additional data if necessary. 	
	• /	Explain the why Inverse kinematics solution is not unique for generic robots. What is the tool configuration vector? Explain its role in the solution of an inverse binary tip problem.	5
		inverse kinematic problem. Define hard/fixed, soft/flexible automation and hence the relative cost effectiveness of different types of automation with a neat sketch.	5
·		Define link and joint kinematic parameters.	5
2. ((a)	Find the joint position of the tool tip of the Adept One robot when the joint variables are $q = [\Pi/4, -\Pi/3, 120, \Pi/2]^T$ Where $d = [877, 0.0, d3, 200]^T$ $a = [425, 375, 0.0, 0.0]^T$.	10
((b)	How does the SCARA arm geometry differ from the vertical articulated arm? Why is the SCARA arm more ideal for assembly applications.	10
		Explain with diagram basic four steps for transferring Frame k-1 to frame k. Explain the inverse arm kinematics of a two DOF cylindrical coordinate robot arm.	10 10
4. ((a) (b)	What is the different between Path & Trajectory? Explain Trajectory planning? Explain the bounded deviation algorithm for straight line motion of the tool path.	10 10
	•	Explain linear interpolation with parabolic blends. Discuss its advantages over piecewise linear interpolation.	10
((b)	Explain role of line and area descriptors for analyzing shape of an object.	10
	-	Explain with ladder diagram PLC system for dispensing oil from tank. What are the advantages and disadvantages of PLC system.	10 10
7.	V	Vrite notes on the following: (a) Classification of robots (b) Template matching technique for part recognition (c) Link co-ordination arm equation (d) Robot specification	20

Q.P. Code: 630801

		(3 Hours) [Total Marks:	100
N.]	B.:	 (1) Question No.1 is compulsory. (2) Attempt any Four questions out of the remaining Six questions. 	
1.	. (Answer the following questions: (a) What is meant by optical networking? what are its advantages and disadvantages? (b) Compare TCP and UDP. (c) Explain fragmentation in 1Pv4 with an example. (d) Explain how Congestion control is implemented in Frame Relay.	20
2.		With a neat schematic diagram, explain the functional layers of SONET and its hardware components. Bring out the functionality of each component. Describe the steps in completing the Access layer design in detail. Compare Ubiquotous and Hierarchical Access network design.	10
3.		Mention the need for network security. Discuss various security threats and safeguards. Explain the IPv4 Datagram. Discuss the strategies for transition from IPv4 to IPv6.	
4.	` '	Explain with a neat diagram, the frame relay format and explain the significance of each field. Compare ATM with Frame relay. Explain the following: (i) Packet filtering and Layer 7 filtering (ii) DMZ.	10
5.		Differentiate between WDM and DWDM. With a neat schematic diagram, explain DWDM technology. Explain with a neat diagram, the the ATM Protocol architecture and explain the functions of ATM layer and Various AAL layers.	
		Explain the meaning of "hidden station" and "exposed station" in WLAN. how it is tackled? With a neat flow chart, explain how CSMA/CA is implemented. Explain RMON and OAM&P.	10
7.	V	Vrite short notes on (Any Four): (a) MAC Sublayer frame format of WLAN (b) Comparison of IPv4 with IPv6 (c) Advantages and limitations of FIREW ALLS (d) Subnetting and Supernetting (e) Comparison of OSI and TCP/IP	20